Programmable Adaptor for Home Applications

Gautham Botlaguduru1, Prof. Ananda M2

1Dept. of Electronics and Communication PESIT-BSC, Bangalore, India
2Asst. Professor, Dept. of Electronics and Communication PESIT-BSC, Bangalore, India

Abstract: In the present scenario of gadget based modern living, portable devices are on high demand. Categorically, they require charging, which in turn demands suitable adaptors. The flipside of an adaptor is its large size and weight which cause lot of inconvenience to the end user. Also, each device has its own specified adaptor, concurrently leading to numerous adaptors lying around. Our project attempts to change the way a switchboard is approached. The user simply chooses an option in the interface provided. Corresponding to this option, a specified voltage and current is provided as the output. The user can connect a conductor from the output port to the device to power it. The switchboard is capable of providing any voltage and current as required by the user. This remodeled switchboard mitigates the inconvenience caused by big and heavy adaptors and proves to be user friendly. It is easy to use and hassle free.

Keywords: Arduino, DAC, Emitter follower(EF), Substractor (SUB), Current source(CS), Current divider (CD), Resister (R).

1. Introduction

In modern living, portable devices are on high demand, they require battery that as to be charged using suitable adaptors. The flipside of an adaptor is its large size and weight which cause lot of inconvenience to the end user. Also, each device has its own specified adaptor, concurrently leading to numerous adaptors lying around and usage becomes cumbersome. And also several techniques have been used to for converting the AC input to required voltage/current to drive for the portable device like Laptop, Mobile, and PDA etc. to be charged to require current.

2. Objective

Our project attempts to change the way a switchboard is approached. The user simply chooses an option in the interface provided. Corresponding to this option, a specified voltage and current is provided as the output. The user can connect a conductor from the output port to the device to power it. The switchboard is capable of providing any voltage and current as required by the user.

3. Hardware and Software Requirements

Hardware requirements:
1. Microcontroller(MC)
2. Digital to Analog Converter (DAC)
3. Common Source (CS)
4. Emitter Follower (EF)
5. Common divider (CD)
6. Substractor (SUB)
7. Resister(R)

4. Block Diagram

![Block Diagram Image]

5. Explanation of Block Diagram

1) Microcontroller
The microcontroller is responsible for generating the required hex value for the DAC and also for controlling the relay switches. The Microcontroller is interfaced with a 4X4 hex keypad. The input chosen is searched by using a defined Look-up table (LUT). The LUT specifies the voltage and current rating of the selected device and necessary bits are generated.

2) Digital to Analog Converter (DAC)
Using the digital data sent by the MC (as per user’s choice) as analog voltage (which is DC in nature) is generated. Since these bits are sent continuously the same
voltage appears at the output acting as the required output voltage.

3) **Current Source (CS)**
   After obtaining the required voltage, we must achieve the required current also. Two current sources, each providing 1A of current, are designed using PNP power transistors. The resistors used in this circuit are also power resistors. In this prototype, we have restricted ourselves to generate 2A of current but it can be extended to achieve higher levels of currents.

4) **Emitter Follower (EF)**
   The current source and emitter follower are connected in series. The Emitter follower is implemented using an NPN transistor. The input to the emitter follower is the obtained DAC voltage and are also controlled by switches.

5) **Common Divider (CD)**
The Common divider is a network of 10 pairs of equivalent resistors connected in parallel. In each pair, the resistors are series with the switch. These switches are always complementary to each. For example, to generate 0.1A, 1A is divided into 10 branches of equal resistors, hence giving us ten 0.1 A branches. Out of these 9 are connected to the output of the subtractor and another branch is connected to the load.

6) **Subtractor (SUB)**
The subtractor is used to ensure that 0.1A flows through each branch that is connected to its output. The output of subtractor is always such that the voltage difference between the resistors is 1V. This ensures flow of 0.1A.

6. **Implementation**

![Prototype overview](image)

**Figure 4: Prototype overview**

(1) DAC output = 3.92 V

(2) Common source current = 0.92A

(3) Emitter follower output = 3.18V

(4) Common Divider current = 0.62V

7. **Comparison Table**

<table>
<thead>
<tr>
<th>Device</th>
<th>Theoretical values</th>
<th>Practical values</th>
</tr>
</thead>
<tbody>
<tr>
<td>DAC</td>
<td>3.75V</td>
<td>3.92V</td>
</tr>
<tr>
<td>CS</td>
<td>1A</td>
<td>0.92A</td>
</tr>
<tr>
<td>EF</td>
<td>3.15</td>
<td>3.18V</td>
</tr>
<tr>
<td>CD</td>
<td>0.7V</td>
<td>0.62V</td>
</tr>
</tbody>
</table>
8. Applications

- Apartments
- Office
- Malls
- Cinema Theaters

9. Conclusion & Further Scope

Here we aimed at arriving the changes of present switchboard were user simply chooses an option in the interface provided. Corresponding to this option, a specified voltage and current is provided as the output. The user can connect a conductor from the output port to the device to power it. The prototype for such a switchboard is implemented and tested.

10. Acknowledgment

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References

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[2] “Operational Amplifiers” by David A bell

Author Profile

Prof. ANANDA M is an associate professor at the Department of Electronics and Communication, PESIT-BSC. His area of specialization is in the area of embedded systems and VLSI. He has 13 years of experience and is currently a faculty member in PESIT-BSC.

Mr. Gautham Botlaguduru is a student in the Department of Electronics and Communication, PESIT-BSC. He is currently in his fourth year.