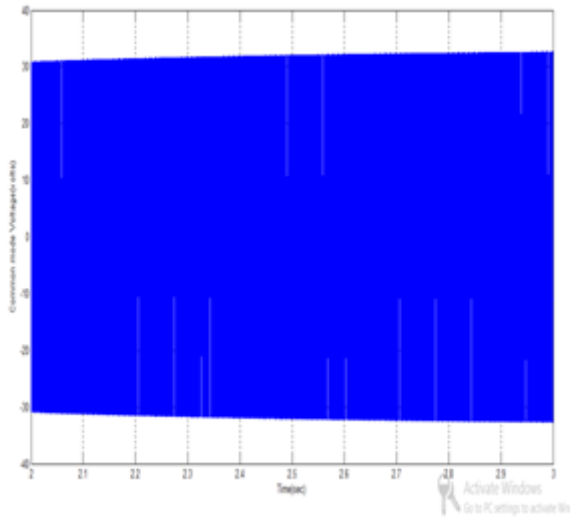

Figure 9: Common Mode Voltage (CMV) SVPWM

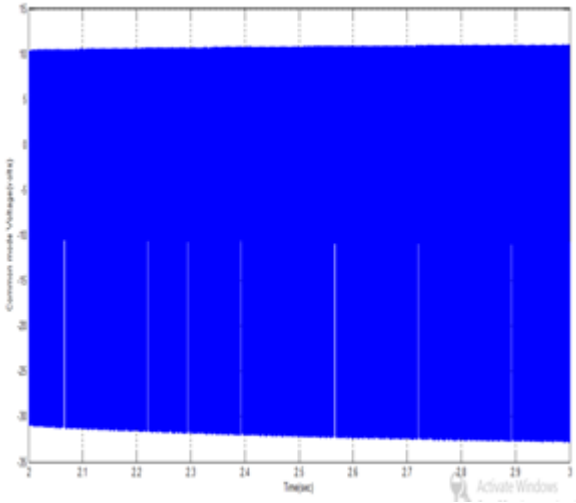

Figure 10: Common Mode Voltage (CMV) DSVPWM

6. Conclusion

By the parallel connection of two rectifiers at the input side, we can reduce the harmonic distortion, rectifier switch currents and improves the fault tolerance characteristics. From the results we can observe that the improvement in the Total harmonic distortion (THD), Power factor, Common-mode Voltage (CMV) and switching losses of the system by using DSVPWM technique compared to SVPWM technique.

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