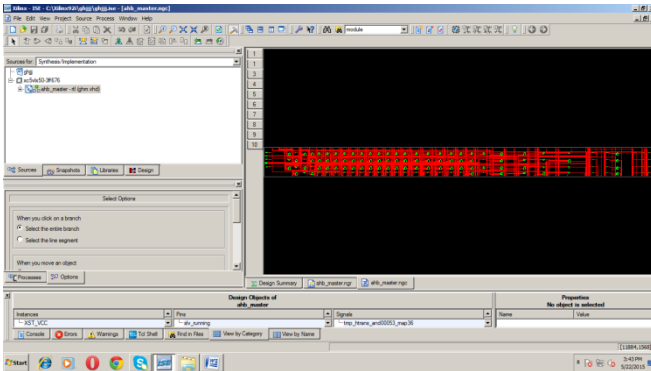
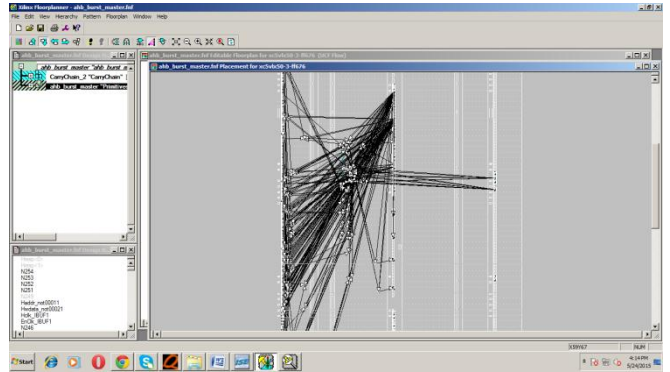


Technological View



	Voltage (V)	Current (mA)	Power (mW)
Vccint	1		
Dynamic		0.00	0.00
Quiescent		227.49	227.49
Vccaux	2.5		
Dynamic		0.00	0.00
Quiescent		57.00	142.50
Vcco25	2.5		
Dynamic		0.00	0.00
Quiescent		1.25	3.13
Total Power			373.12



Device Utilization Summary				
Slice Logic Utilization	Used	Available	Utilization	Note(s)
Number of Slice Registers	126	28,800	1%	
Number used as Flip Flops	126			
Number of Slice LUTs	386	28,800	1%	
Number used as logic	352	28,800	1%	
Number using O6 output only	301			
Number using O5 output only	35			
Number using O5 and O6	16			
Number used as Memory	32	7,680	1%	
Number used as Dual Port RAM	32			
Number using O5 and O6	32			
Number used as exclusive route-thru	2			
Number of route-thrus	37	57,600	1%	
Number using O5 output only	37			
Slice Logic Distribution				
Number of occupied Slices	193	7,200	2%	
Number of LUT Flip Flop pairs used	400			
Number with an unused Flip Flop	274	400	68%	
Number with an unused LUT	14	400	3%	
Number of fully used LUT-FF pairs	112	400	28%	
Number of unique control sets	7			
IO Utilization				
Number of bonded IOBs	306	440	69%	
IOB Flip Flops	13			
Specific Feature Utilization				
Number used as BURGS/BURGCTRLs	1	32	3%	
Number used as BURGS	1			
Total equivalent gate count for design	11,883			
Additional JTAG gate count for IOBs	14,688			

Power Consumption

