A Review Paper on Frequency Compensation of Transconductance Operational Amplifier (OTA)

Raghavendra Gupta¹, Prof. Sunny Jain²

Scholar in M.Tech in LNCT, RGPV University, Bhopal M.P. India¹ Asst. Professor in Department of Electronics & Communication in LNCT, RGPV University Bhopal, M.P. India² *Email: theraghav.india@gmail.com¹*, sunnyjain2008@gmail.com²

Abstract: In this brief, types of frequency compensation for operation transconductance amplifier are explained in detail. Compensation which is used to enhance frequency stability of the OTA, and to make gain and phase linear for required frequency range. As we know capacitor inside the OTA causes output to lag behind by 90° with each pole they create, If the addition of these phase lags add-up to 360°, the output signal will be in phase with the input signal. Feeding output signal back in proportion to input when the gain of the amplifier is sufficient will cause the amplifier to oscillate. To avoid such undesirable condition frequency compensation is required in OTA, the frequency compensation is used to make a particular pole dominant on other poles to make the transfer function 2nd order.

KEY WORDS: CMOS technology, operational amplifier, operational trans-conductance amplifier (OTA), Gain Bandwidth Product (GBW), Phase margin, positive feedback, gain enhancement.

1. INTRODUCTION

In electronics industry operational amplifier are the basic building blocks to develop analog environment for analog circuits. Generally amplifiers use negative feedback for its frequency stability, to avoid unwanted creation of positive feedback which causes amplifier to oscillate. The negative feedback controls the overshoot ringing of amplifier and it's bandwidth. Gain bandwidth product is very crucial parameter which shows the stability of the amplifier over the desired frequency range. Frequency compensation is a technique which uses pole splitting method to make the transfer function of single order to 2nd order with a dominant pole. Wide band amplifiers are most widely used for pulse and video amplification in communication systems and video display units. Some of the present non-feedback bandwidth-enhancing techniques are: cascoding [11], Cc-cancellation [2], [3] and "parasitic capacitance compensation" [4] techniques. Cascode and Cc-cancellation techniques prevent bandwidth reduction caused by "Miller effect," but as mentioned in later part better frequency responses are achievable in an amplifier. "Parasitic capacitance compensation" is a technique for cancelling undesired capacitors of an amplifier. However, frequency limitation of active compensating network does not permit an exact cancellation. Circuits described which are also mainly applicable to low-gain amplifiers. A new technique is

introduced in this paper based on an effect similar to the one occurring in the resistive feedback amplifiers.

2. DOMINANT-POLE COMPENSATION

Dominant pole compensation is the most widely used method for frequency compensation. It is a form of lag compensation, in this compensation a low frequency pole will be made dominant by adjustment of low frequency pole in a manner that other higher poles are to be made very far from the dominant pole, so that the transfer function (gain vs frequency curve) crosses the 0db line in between the dominant pole and other poles. Generally the lowest frequency is set to be dominant, so that it dominates the effect of higher frequency poles which reduces phase margin. It will make the difference between "open loop output phase and closed loop phase response of feedback network, not to falls below -180⁰ with maintaining gain constant.

General purpose operational trans conductance amplifier uses dominant pole compensation by adding capacitor in between the gain stages of OTA. Adding capacitor introduces pole which is adjusted at low frequency so that gain vs frequency curve cross 0db just after the low frequency which is considered as dominant, this result, phase margin of 45° approximately depending upon the location of other or next higher poles. This phase margin is sufficient to control the oscillation, in commonly used feedback system. The dominant pole compensation has other advantage such as, it controls the overshoot and ringing of the OTA when we apply to step input. It is the most demanding requirement in terms of stability of OTA.

This type of frequency compensation passes through two drawbacks:

- **2.1** At high frequency the bandwidth of the OTA reduces thereby reducing open loop gain, this will affect in amount of feedback which causes distortion at such high frequency.
- **2.2** Dominant pole compensation technique reduces the slew rate of the OTA. This is caused by current driving stage which charge and discharge the compensated capacitor, this limits the fast-changing output of the OTA.

Pole splitting technique is used in dominant pole compensation. This will move the lower frequency of uncompensated amplifier towards origin to make it dominant and other higher frequency poles were move to a higher frequency.

3. MILLER COMPENSATION TECHNIQUE:

Miller compensated two stage OTA which is compensated by C_M capacitor to enhance the frequency response is shown in figure 1 and figure 2 shows its small signal model for frequency analysis.



Fig.1 Compensated two-stage OTA



Fig.2 Small signal model of compensated two-stage OTA

from equation (1) & (2)

17

$$V_0 \left[s(c_2 + c_c) + \frac{1}{R_2} \right] \\= \frac{(V_0 s c_c R_1 - g_{m1} V_{in} R_1)(s c_c - g_{m2})}{1 + s R_1 (c_1 + c_c)}$$

$$\begin{split} & \frac{v_0}{v_{in}} \\ &= \frac{g_{m1}g_{m2}R_1R_2(1 - sc_c/g_{m2})}{s^2[\left(R_2R_1(c_1c_2 + c_1c_c + c_2c_c)\right)]s[R_2(c_2 + c_c) + R_1(c_1 + c_c) + c_cg_{m2}R_1R_2] + 1} \\ & T(s) = \frac{A_{DC}\left(1 - \frac{S}{Z}\right)}{\left(1 + \frac{S}{P_1}\right) + \left(1 + \frac{S}{P_2}\right)} = \frac{A_{DC}\left(1 - \frac{S}{Z}\right)}{1 + S\left(\frac{1}{P_1} + \frac{1}{P_2}\right) + S^2\left(\frac{1}{P_1P_2}\right)} \\ & s\left(\frac{1}{P_1} + \frac{1}{P_2}\right) \approx \frac{S}{P_2} \quad \text{as } P_2 \text{ is large enough} \\ & \frac{1}{P_1} = coefficents \ of \ "s" & \frac{1}{P_1P_2} = coefficents \ of \ "s^2" \\ & P_1 \approx \frac{1}{R_2(c_2 + c_c) + R_1(c_1 + c_c) + c_cg_{m2}R_1R_2} \end{split}$$

Volume 4 Issue 4, April 2015 <u>www.ijsr.net</u> Licensed Under Creative Commons Attribution CC BY

$$P_1 P_2 = \frac{1}{R_1 R_2 (c_1 c_2 + c_1 c_c + c_2 c_c)}$$
$$P_2 = \frac{c_c g_{m2} R_1 R_2}{R_1 R_2 (c_1 c_2 + c_1 c_c + c_2 c_c)} \approx P_2 = \frac{c_c g_{m2}}{(c_c c_2)} \approx \frac{g_{m2}}{(c_c c_2)}$$

and zero location $Z = \frac{g_{m2}}{(c_c)}$

& Dc gain $A_{DC} = g_{m1}g_{m2}R_1R_2$

Figure 3 shows the frequency response of compensated and Uncompensated of OTA



Fig 3. Uncompensated & compensated OTA, gain and phase response

4. MILLER COMPENSATION WITH NESTED RESISTANCE :

Miller compensated nested resistance OTA is shown in figure 4 and its small signal model is shown in figure 5.



Fig.4 compensated two-stage OTA with nested resistance



Fig.5 Small signal model of Compensated two-stage OTA with nested resistance OTA

 $C_{01} \& r_{01}$ is the capacitance and resistance of first stage of OTA and g_{m1} is the transconductance of first stage. $C_{02} \& r_{02}$ is the capacitance and resistance of second stage of OTA with g_{m1} is its transconductance $C_L + C_2 \approx C_L$ as C_2 is very large. The approximate expression for pole and zero are shown below from equation [2]. Due to nested miller resistance the transfer function is of third order,

$$Z_{1} \approx \left((g_{m2}^{-1} - R_{M})C_{M} \right)^{-1}$$
$$P_{1} \approx (-g_{m2}r_{02}r_{01}C_{M})^{-1}$$
$$P_{2} \approx -g_{m2}(C_{L})^{-1}\&P_{3} \approx (C_{M}R_{M})^{-1}$$

ł

The first pole P1 is at low frequency which set the GBW of the OTA, the zero Z1 cancel out P2 with small and fixed load C_L and P2 & P3 are very far away from P1, so P1 will be the dominant pole to stabilize the frequency, there is limitation on achievable phase margin since increasing Rm not only tunes Z1 to cancel out P2 but it also reduces the third pole when C_L is not fixed, or very large then the phase margin we get is not up to the required level this will create worst condition, as to achieve the required phase margin we have to increase the compensation capacitor, [12]. Use of large capacitive load unfortunately reduces the GBW and other drawback of such compensation is the process variation.

5. ENHANCEMENT PHASE COMPENSATION

It is clear that Miller compensation improves the phase margin by reducing gain bandwidth, try to increase the gain reduces the phase margin and made the pole complex [3],[4].A positive feedback at the output gain stage increases the gain& bandwidth, based on this approach enhanced phase compensation work as shown in figure 6.

International Journal of Science and Research (IJSR) ISSN (Online): 2319-7064 Index Copernicus Value (2013): 6.14 | Impact Factor (2013): 4.438



Fig.6 Phase enhanced compensated two-stage OTA

Where two zero are introduced by positive feedback using register and capacitor, this will increase a pole also, to make the transfer function fourth order, but due to Miller dominant pole condition two zero cancel out two poles to make it second order transfer function. Small signal model is shown in figure 7.



Fig.7 Small signal model of Phase enhanced Compensated two-stage OTA

The positive feedback RC cross-linked couple is made to increase the gain of the OTA and to stabilize it the transfer function. Transfer function is given by the following equations given below [1], and pole and zero approximated from the transfer function, as it is seen that the DC gain of the OTA is unchanged as RC link present an open circuit. Using KCL and KVL.

 $\frac{V_{out}}{V_{in}} = g_{m1}g_{m2}r_{01}r_{02} \times$

 $\frac{(g_{m2}^{-1}(C_1C_2R_1 - C_1C_2R_2) + R_1R_2C_1C_2)S^2 + (g_{m2}^{-1}(C_2 - C_1) + C_1R_1 + C_2R_2)S + 1}{(C_{01}C_LC_1C_2r_{01}r_{02}R_1R_2)S^4 + D_1S^3 + D_2S^2 + D_3S + 1}$

$$\begin{split} D_1 &= C_{01}C_LC_1r_{01}r_{02}R_1 + C_{01}C_LC_2r_{01}r_{02}R_2 \\ &\quad + C_{01}C_1C_2r_{01}r_{02}R_1 + C_LC_1C_2r_{01}r_{02}R_1 \\ &\quad + C_{01}C_1C_2r_{01}r_{02}R_2 + C_LC_1C_2r_{01}r_{02}R_2 \\ &\quad + C_{01}C_1C_2r_{02}R_1R_2 + C_LC_1C_2r_{01}r_{02}R_1 \\ \end{split}$$

$$+ C_{01}C_LC_1C_2r_{01}r_{02}R_2g_{m2}$$

 $-C_{01}C_LC_1C_2r_{01}r_{02}R_1g_{m2}$

$$D_3 = C_{01}r_{01} + C_Lr_{02} + C_1r_{01} + C_1r_{02} + C_2r_{01} + C_2r_{02} + C_1R_1 + C_2R_2 + g_{m2}C_1r_{01}r_{02} - g_{m2}C_2r_{01}r_{02}$$

$$\begin{split} & Z_1 \approx -\frac{1}{C(R_1 + R_2)} \\ & Z_2 \approx -\frac{R_1 + R_2}{C(R_1 R_2)} \\ & P_1 \approx -\frac{1}{(C_L + 2C)r_{02} + 2Cr_{01}} \\ & P_2 \approx -\frac{(C_L + 2C)r_{02} + 2Cr_{01}}{2C_L r_{02}r_{01} + 4r_{02}r_{01}C^2} \\ & P_3 \approx -\frac{2C_L + 4C}{CC_L(R_1 + R_2)} \& P_4 \approx -\frac{R_1 + R_2}{C_{01}(R_1 R_2)} \\ & R_1 + R_2 \approx \frac{2C_L r_{02}r_{01} + 4Cr_{02}r_{01}}{C_L r_{02} + 2C(r_{02} + r_{01})} \\ & \alpha / (1 + \alpha)^2 \approx \frac{C_L}{2C_L + 4C} \\ & BW_{RATIO} \approx \frac{(C_L + C_M)r_{02} + g_{m2}r_{02}r_{01}C_M}{(C_L + 2C)r_{02} + 2Cr_{01} + C(R_1 + R_2)} \\ & GBP_{RATIO} \approx \frac{(R_1 + R_2)C_L}{C_{01}R_1 R_2 g_{m2}} \end{split}$$

Figure 8 shows frequency response for the Miller compensation and the Phase enhanced compensation.

Volume 4 Issue 4, April 2015 <u>www.ijsr.net</u> Licensed Under Creative Commons Attribution CC BY





6. CONCLUSION

Three types of compensation technique is discussed in this paper and it shows that the phase enhance compensation is better than miller compensated technique in respect to phase enhance. The compensation technique provides improved bandwidth and gain bandwidth in caparison to uncompensated OTA.

REFERENCES

- Mohammed Abdulaziz, Markus Törmänen and Henrik Sjöland, IEEE "A Compensation Technique for "Two-Stage Differential OTAs" IEEE Transactions On Circuits And Systems, vol. 61, no. 8, august 2014.
- [2]. M. Abdulaziz, A. Nejdel, M. Törmänen, and H. Sjöland,"A 3.4 mw 65 nm CMOS 5th order programmable active-

RC channel select filter for LTE receivers," in Proc. IEEE RFIC, 2013, pp. 217–220.

- [3]. P. Gray, P. J. Hurst, S. H. Lewis, and R. G. Meyer, Analysis and Design of Analog Integrated Circuits. Hoboken, NJ, USA: Wiley, 2010.
- [4]. M. Vadipour, "Capacitive feedback technique for wideband amplifiers," IEEE J. Solid-State Circuits, vol. 28, no. 1, pp. 90–92, Jan. 1993.
- [5]. A. Vasilopoulos, G. Vitzilaios, G. Theodoratos, and Y. Papananos, "A low-power wideband reconfigurable integrated active-RC filter with 73 dB SFDR," IEEE J. Solid-State Circuits, vol. 41, no. 9, pp. 1997–2008, Sep. 2006.
- [6]. G. Palmisano, G. Palumbo, and S. Pennisi, "Design procedure for twostage cmos transconductance operational amplifiers: A tutorial," Analog Integr. Circuits Signal Process., vol. 27, no. 3, pp. 179–189, May 2001.
- [7]. K. N. Leung, P. K. T. Mok, W.-H. Ki, and J. K. O. Sin, "Three-stage large capacitive load amplifier with dampingfactor-control frequency compensation," IEEE J. Solid-State Circuits, vol. 35, no. 2, pp. 221–230, Feb. 2000.
- [8]. H.-T. Ng, R. M. Ziazadeh, and D. J. Allstot, "A multistage amplifier technique with embedded frequency compensation," IEEE J. Solid-State Circuits, vol. 34, no. 3, pp. 339–347, Mar. 1999.
- [9]. B. Thandri and J. Silva-Martinez, "A robust feedforward compensation scheme for multistage operational transconductance amplifiers with no miller capacitors," IEEE J. Solid-State Circuits, vol. 38, no. 2, pp. 237–243, Feb. 2003.
- [10].X. Fan, C. Mishra, and E. Sanchez-Sinencio, "Single miller capacitor frequency compensation technique for low-power multistage amplifiers," IEEE J. Solid-State Circuits, vol. 40, no. 3, pp. 584–592, Mar. 2005.
- [11].H. Lee, K. N. Leung, and P. K. T. Mok, "A dual-path bandwidth extension amplifier topology with dual-loop parallel compensation," IEEE J. Solid- State Circuits, vol. 38, no. 10, pp. 1739–1744, Oct. 2003.
- [12].A. Grasso, G. Palumbo, and S. Pennisi, "Three-stage CMOS OTA for large capacitive loads with efficient frequency compensation scheme," IEEE Trans. Circuits Syst. II, Exp. Briefs, vol. 53, no. 10, pp. 1044–1048, Oct. 2006.

- [13].R. Assaad and J. Silva-Martinez, "The recycling folded cascode: A general enhancement of the folded cascode amplifier," IEEE J. Solid-State Circuits, vol. 44, no. 9, pp. 2535–2542, Sep. 2009.
- [14].N. Krishnapura, A. Agrawal, and S. Singh, "A high-IIP3 third-order elliptic filter with current-efficient feedforwardcompensated opamps," IEEE Trans. Circuits Syst. II, Exp. Briefs, vol. 58, no. 4, pp. 205–209, Apr. 2011.
- [15].M. Ahmadi, "A new modeling and optimization of gainboosted cascode amplifier for high-speed and low-voltage applications," IEEE Trans. Circuits Syst. II, Exp. Briefs, vol. 53, no. 3, pp. 169–173, Mar. 2006.
- [16].Phuoc T. Tran, Herbert L. Hess, Kenneth V. Noren Operational Amplifier Design with Gain-Enhancement Differential Amplifier 978-1-4673-2421-2/12/\$31.00
 ©2012 IEEE.

Author's Profile

Raghavendra Gupta has received his Bachelor of Engineering degree in Electronics & communication engineering from Oriental College of Technology, Bhopal in the year 2010. At present he is pursuing M.Tech with the specialization of VLSI Design in LNCT, Bhopal. His area of interest Amplifier design, VLSI designing field & Programming Languages.

Prof. Sunny Jain has received his Bachelor of Engineering in Electronics & Instrumentation Engineering from JNCT, Bhopal in the year 2009 & has received his M.Tech from OIST, Bhopal with the specialization of Digital Communication. At present he is working as an Ass. Professor at LNCT, Bhopal. His areas of interests are Power electronics, Digital Communication Field.