

Figure 7.2: Output waveform of the switch s2 in the inverter

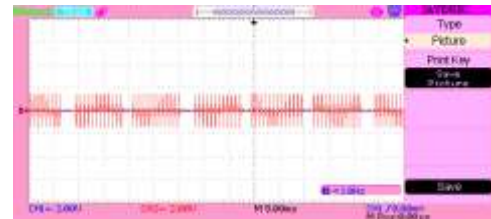


Figure 7.8: Inverter output at normal voltage

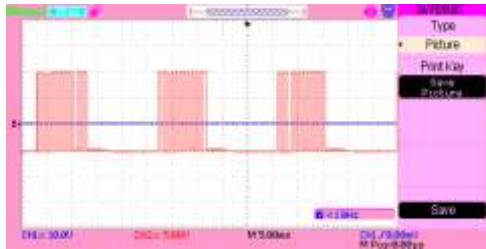


Figure 7.3: Output waveform of the switch s3 in the inverter

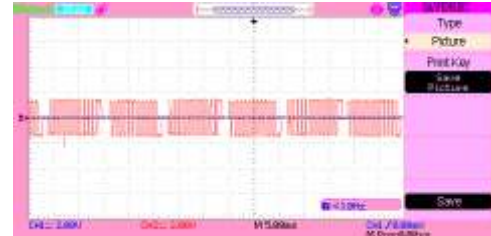


Figure 7.9: Inverter injection output at sag voltage

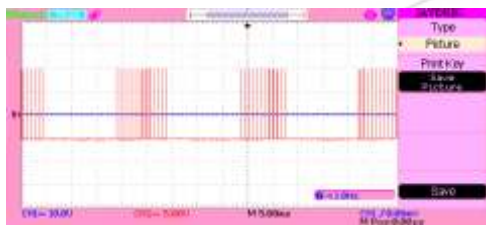


Figure 7.4: Output waveform of the switch s4 in the inverter

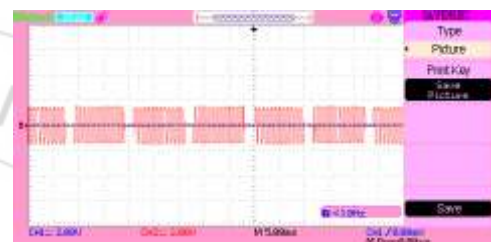


Figure 7.10: Inverter injection output at swell voltage.

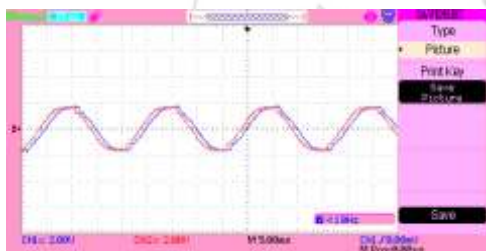


Figure 7.5: Input voltage and set voltage in PIC controller, normal voltage

### 7.1 Applications of the proposed system

- 1) LED lighting
- 2) Hybrid vehicle
- 3) Battery charging
- 4) Telecommunications
- 5) Power factor improvement

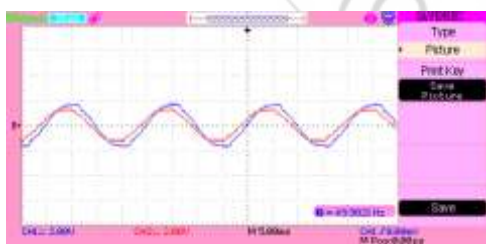


Figure 7.6: Input voltage and set voltage in pic controller, sag voltage

## 8. Conclusions and Future scope

### 8.1 Conclusion

In today's world, electric power plays a vital role. The world is unimaginable without electric power. How far it is used, the risk of electric power gets increases due to voltage sag and swell. Hence this project paves a better way to mitigate the risk due to sag and swell voltage. The Dynamic Voltage Restorer (DVR) is an effective device for power quality enhancement due to its quick response and high reliability. The conclusion is that it is an effective apparatus to protect sensitive load from short duration of voltage sag and swell. The effectiveness of the DVR depends upon rating of energy storage device and loads. In this project, Magnitude tracking is the method to mitigate the voltage sag and swell. The main advantage of this project is there low switching power loss, low cost and its control is simple. It can mitigate long duration voltage sags/swells efficiently. Future work will include a comparison with a laboratory experiments in order to compare simulation and experimental results.

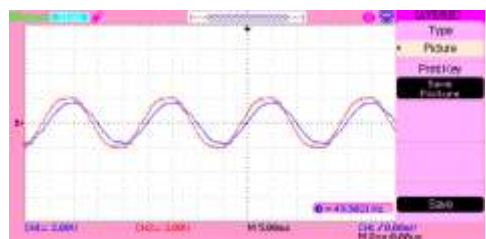


Figure 7.7: Input voltage & set voltage in PIC controller, swell voltage

## 8.2 Future scope

In future the proposed circuit diagram can be implemented in.,

- Solar energy system.
- Wind energy system.
- Power factor correction.
- Telecommunication system(the voltage is 48V dc.)

## References

- [1] MS.S.P.Awate “Enhancement of voltage profile using dynamic voltage restorer”. IJAREEIE(volume-2,issue-12,dec 2013).
- [2] Priyanka kumara vijyakumar Garg “power quality enhancement using dynamic voltage restorer” IJSRP (volume-3,issue-8,august 2013)
- [3] Risha dastagir,Mariam asif “power quality improvement using dynamic voltage restorer” IJRDET(volume-2,issue-5,may 2014).
- [4] M.Sharanya,B.basvaraja,M.Sasikala “An overview of DVR for voltage profile improvement” (volume-2,issue-2,dec 2012).
- [5] Subhro paul,Sanjay Sarkar,Surojit sarkar,pradip kumar saha,Gautam kumar panda “By dynamic voltage restorer for quality improvement” (volume-2,issue-1,jan 2013).
- [6] M. H. Ali and B. Wu, “Comparison of stabilization methods for fixed speed wind generator systems,” IEEE Trans. Power Del., vol. 25, no. 1, pp. 323–331, Jan. 2010.
- [7] R. K. Smith et al., “Solid state distribution current limiter and circuit breaker: Application requirements and control strategies,” IEEE Trans. Power Del., vol. 8, no. 3, pp. 1155–1162, Mar 93. 1
- [8] A. M. S. Atmadji and J. G. J. Sloot, “Hybrid switching: A review of current literature,” in Proc. Energy Manage. Power Del. (EMPD), Singapore, 1998, pp. 683–688.
- [9] A. Abramovitz and K. M. Smedley, “Survey of solid-state fault current limiters,” IEEE Trans. Power Electron., vol. 27, no. 6, pp. 2770–2782, Jun. 2012.
- [10] A. R. Fereidouni, B. Vahidi, and T. H. Mehr, “The impact of solid state fault current limiter on power network with wind-turbine power generation,” IEEE Trans. Smart Grid, vol. 4, no. 2, pp. 1188–1196, Jun. 2013.