Design a Uplink-Downlink Reconfigurable Patch Antenna for Modern Space Application

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Abstract: In this paper, a modified elliptical reconfigurable patch antenna with stripline feed has been analyzed and simulated for the modern satellite communication applications. The overall size of the antenna is 60mm × 60 mm × 1.59 mm. The antenna has been operated on two different resonant frequency of 3.93 GHz (downlink) and 6.15 GHz (uplink) for satellite and application band on the basis of switching conditions. The simulated gain of antenna in the obtained frequency range is close to 4.7 dBi and 3.6 dBi for OFF and ON conditions respectively. Maximum radiations are directed normal to patch geometry and shape of patterns is somewhat similar to an isotropic radiation pattern [4] as a dumbbell shape in the upper hemisphere. This reconfigurable patch antenna has been designed specifically for the application of satellite communication systems. The proposed antenna has been simulated by applying CST Microwave Studio Software.

Keywords: Reconfigurable, Compact, PIN-diode, CST Microwave

1. Introduction

A reconfigurable antenna can be considered as one of the key elements in future satellite communication transceivers. The benefits of using a reconfigurable antenna is the ability to operate in two different frequency bands where the total antenna volume can be reused thus enabling the overall size to be reduced. Devices using a single compact antenna allow reduction in the dimensions of the device and more space to integrate other electronic components.

In addition, reconfigurable antenna [3] can be a cheaper alternative to traditional adaptive arrays or they can be incorporated into adaptive arrays to improve their performance by providing additional degrees of freedom. Microstrip Patch antenna has played an important role in improving the performance and reducing the overall size of satellite devices by observing the inherent properties of patch antenna. Now a day antenna designers are modifying the conventional geometries, so that these may be applied in modern communication systems. It is realized that using a slot/slits in the patch geometry or modifications in substrate parameters improves the performance of antenna to a great extent but after reaching an optimum value, no feasible improvement in performance may be achieved. Pertaining to satellite communications, an uplink (UL or U/L) is the part of a communication link used for the transmission of signals from an Earth terminal to a satellite or to an airborne platform. A downlink (DL) is the link from a satellite to a ground station.

In recent times several single layer and rectangular geometry fed by microstrip line have been reported [1-2] which mainly resonate in higher frequency. In the present paper a compact reconfigurable patch antenna fed by strip line has been designed. A conventional patch antenna has been modified in several steps and radiation performances in each stage of modification are simulated using CST Microwave Studio Simulation Software.

2. Antenna Design and Analysis of the Results

The simple elliptical patch antenna [1] consists of a dielectric material FR4 lossy for substrate layer having dimensions 60x60x1.59mm. The simple elliptical patch antenna is shown in figure:

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Figure 1: Top and Bottom view of Elliptical patch Antenna

The results shown below, Relation between S- parameter magnitude in dB and frequency in GHz, indicates that conventional elliptical patch antenna is resonating at 3.9835 GHz with narrow Impedance Bandwidth.
The figure 3 shows the relation between Standing Wave Ratio and Frequency. Result shown below, the Antenna SWR is 1.0384 which is less than 2 at resonating frequency 3.9835GHz.

The figure 4 shows the impedance matching between patch and feed-line geometry. In this Smith chart graph, input impedance value of antenna at resonance frequency 3.985 GHz is 51.25 real, -1.437209 Ohm which is close to 50 Ohm. In this way, a very good matching between the designed antenna structure and the few network has been achieved.

This modified antenna geometry resonate at 6.15 GHz frequency. The Antenna SWR is 1.4348 which is less than 2 at resonating frequency 6.15 GHz. The gain of modified antenna geometry is 4.7dBi. This antenna geometry is suitable for satellite uplink application.
Figure 7 shows the circuit diagram of Reconfigurable Antenna [11] in which the PIN Diode is connected with Inductors and Resistors. The PIN Diode is turned ON/OFF using a DC biased signal. The bias network for diode is shown in figure 7(b), where Inductors L1 and L2 are used as RF chokes to provide low impedance for DC signals and high impedance for RF signals. The resistors R1 and R2 are used to provide DC biasing current to PIN Diode.

In next condition PIN diode [9-10] works as a Forward bias (ON condition). Figure 8 shows the antenna geometry in the ON condition. In the Forward bias condition (ON condition), the modified antenna geometry resonate on the lower side which reside on the downlink frequency band 3.7 to 4.2GHz.

This modified antenna geometry resonates at 3.93 GHz frequency. The Antenna SWR is 1.1254 which is less than 2 at resonating frequency 3.93 GHz. The gain of modified antenna geometry is 3.6dBi. This antenna geometry is suitable for satellite downlink application.

Table1 shows the resonant frequencies in ON and OFF conditions.

<table>
<thead>
<tr>
<th>The state of Switches</th>
<th>Resonant Frequencies f(GHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>OFF</td>
<td>6.15</td>
</tr>
<tr>
<td>ON</td>
<td>3.93</td>
</tr>
</tbody>
</table>

Table 2: antenna parameters in ON and OFF condition

<table>
<thead>
<tr>
<th>Parameter</th>
<th>ON</th>
<th>OFF</th>
</tr>
</thead>
<tbody>
<tr>
<td>S- parameter</td>
<td>-24.5dB</td>
<td>-14.91dB</td>
</tr>
<tr>
<td>VSWR</td>
<td>1.124</td>
<td>1.438</td>
</tr>
<tr>
<td>Gain</td>
<td>3.6dBi</td>
<td>4.7dBi</td>
</tr>
</tbody>
</table>

Figure 9 illustrates the relation between magnitude of S-parameter (dB) and frequency (GHz). When Switch is OFF, then obtained resonance frequency is 6.15 GHz, means Reconfigurable antenna works at Uplink Frequency. When Switch is ON then obtained resonance frequency is 3.93, means Reconfigurable Antenna work at Downlink Frequency.

Figure 10 shows the relation between gain in dB and frequency. In this graph represent both ON and OFF condition. In first graph antenna work as a switching ON state, this condition is obtained gain is 3.6dBi at resonating frequency.
frequency 3.93 GHz. In second graph antenna works as a Switching OFF state. For this condition, the obtained gain is 4.7dBi at resonating frequency 6.15 GHz.

Radiation Pattern is very important parameter for antenna. The far field radiation pattern of the Reconfigurable Antenna is similar to isotropic radiation pattern Figure 11 and 12 demonstrate the ON and OFF switching condition. The antenna radiation pattern looks like a dumble shape at Uplink (6.15GHz) and Downlink (3.93GHz).

3. Conclusion and Future Work

This paper presents the design and performance of a modified reconfigurable elliptical patch antenna for satellite downlink-uplink application. The combined effect of modifications in patch geometry has significantly reconfigured the antenna operating frequency in two different operating bands for satellite or space application. The antenna gain is 3.6 dBi for downlink resonant frequency and 4.7dBi for uplink frequency application. In E plane, simulated far-field radiation patterns have been obtained at several frequencies and at the frequency of 6.15GHz maximum radiations are directed normal to patch geometry and shape of patterns is more or less similar to a dumble shape and semi hemispherical shape. This antenna is specifically designed for satellite communication systems. The frequency range of 3.4 - 4.2 GHz is used for Fixed Satellite Service (FSS) and broadcast satellite service (BSS) downlinks, Whereas 5.9 - 6.4 GHz frequency band is used for the FSS/BSS uplinks [8].

We can also improve or enhance the bandwidth of the proposed antenna in future to perform in a wider frequency band.

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References


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NAMAN MEHROTRA received the B.Tech. Degree in Electronics and Communication Engineering from the University of UTTAR PRADESH, Sultanpur, U.P, in 2013, the M.Tech. Degree in Digital Communication Engineering from the JECRC University, Jaipur, Rajasthan, in 2015. Currently, He is an assistant Professor of Eletronics and Communication Engineering at JECER UDML College of Engineering, Jaipur RTU. His teaching and research areas include antenna system and optical communication.