

Multiple Error Recovery in FIR Filter using Reduced-Precision Redundancy (RPR)

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Abstract: In this paper, Reduced-Precision redundancy (RPR) technique is used to protect FIR Filter against single event upsets (SEUs). One of the radiation effect is Single Event Upset (SEU) which causes a logical bit changing in a device and results in an unrecoverable error in a system. RPR is a redundancy technique similar to TMR that requires less hardware overhead by using reduced-precision modules in two of its three replicas [1]. FIR Filters are common in the analog and digital communications. This paper introduces the RPR Technique to protect the FIR Filter and this is implemented in order to provide lower power consumption & reduced complexity. The performance of the proposed system is compared with SCTMR & SMERTMR in terms of hardware requirements. The proposed method is designed using VHDL on Xilinx ISE simulator

Keywords: SEUs, RPR, FIR, Multiple error recovery, TMR Systems

1. Introduction

In space systems, radiation sources are abundant. The effects of radiation are hard to predict and the combination of effects can destroy a system. The Single Event Upset (SEU) is a radiation effect, which causes a bit changing in a device. A logical bit changing may cause a chain reaction and consequently result in an unrecoverable error of a system.

The Digital Filters are commonly used in analog and digital communication to remove the unwanted noise in the signal thereby providing better quality at the output. As communications are fundamental to space borne applications, digital filters play an important role in space systems such as satellites, unmanned missions, etc [5]. The digital filters produce stabilized signal when compared to the analog filters. So digital filters are more used than the analog one. Transposed-form FIR Filter is generally selected than direct filter because of its lower complexity, higher performance and power efficiency (fig. 1)

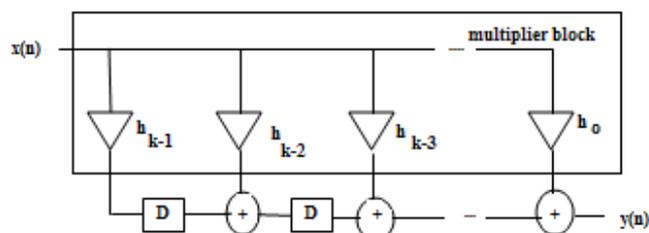


Figure 1: Transposed FIR Filter

Therefore, different methods are presented to protect the FIR Filters from single event upset and to satisfy timing constraints such as triple modular redundancy, SCTMR and SMERTMR and using RPR Technique.

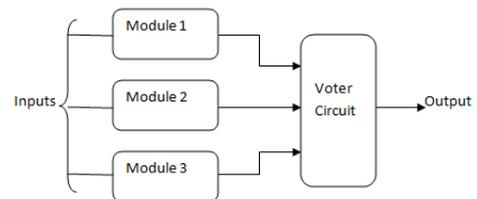


Figure 2: Triple Modular Redundancy

Triple Modular Redundancy (TMR)(see fig.2) is proposed by Von Neumann where a module is replicated three times and the output is taken from the majority voter. Whenever an error is detected, the voter in the TMR system will take majority of three outputs. However, TMR system fails when there are multiple faults and a faulty module. Also one of the important drawbacks is area overhead because a same module is designed thrice. To overcome the disadvantages of TMR system, the same TMR system is provided with an additional controller where it can detect and recover the faulty module and it is named as SCTMR. But it fails to detect the multiple faulty modules and latent faults. So SMERTMR is designed such that the same system is modified to detect and recover multiple faulty modules and latent faults [8, 10].

To minimize the area overhead and to increase the performance, another technique that is RPR (Reduced Precision Redundancy) is used in the same system to detect the multiple faults. This technique uses two reduced precision modules, one fully precised module out of three modules and decision blocks and the outputs are also replicated three times to avoid single points of failure in those modules. It takes advantage of the fact that RP arithmetic can be a good estimate of computations that use higher precision. It has an advantage of low area overhead and limits the error at the output of a module [1].

2. Literature Survey

2.1 SCTMR Technique

To overcome the drawbacks of TMR system, the same system is provided with an additional controller where it can detect and recover the faulty module. Block diagram of ScTMR (fig .3) consists of three redundant modules, voter with an additional controller. When the voter detects an error, the controller will identify the type of error and recover the state of faulty module by copying the output of the fault free module to the faulty module. The recovery process is done through the scan-chain inputs, scan-chain outputs, and scan-chain enable signals instructed by the ScTMR controller [10].

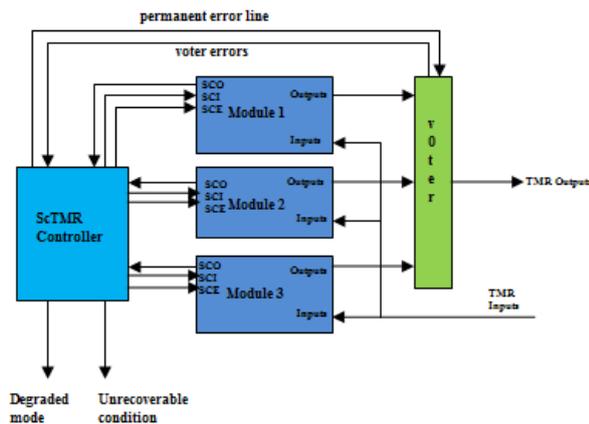


Figure 3: Block diagram of ScTMR

The state diagram of the ScTMR system is shown in (fig .4). This technique consists of four modes of operation. Initially the system will be in normal state. When the voter detects an error, it will go to recovery state. In recovery state, controller will detect the faulty module and the error type. If the error is transient, then the output of the fault free is copied to the faulty module. MRFM (most recent faulty module) holds the faulty module number. This number is equal, then the NCF (number of consecutive faults) will increment by one otherwise it resets to zero. Whenever the value exceeds the threshold value, the module is considered as permanent. If the fault is permanent, then the system will go to master/checker state. Here that module will be discarded and the other two modules will determine the output. If there are multiples faults, then the system will go to unrecoverable condition.

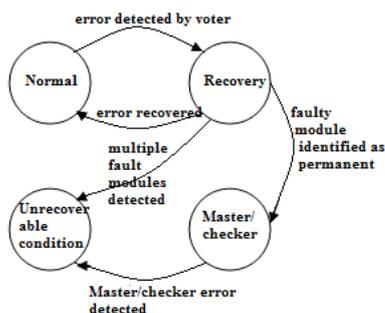


Figure 4: ScTMR state diagram

However, there are disadvantages such as it cannot detect the latent faults and cannot recover from the multiple faults. Latent faults means a fault is not propagated to the system output but causes a mismatch at the TMR modules.

2.2 Proposed Voter

The voter will identify the faulty module. This voter can be used for both the ScTMR and SMERTMR systems. The proposed voter is shown in the (fig .5). There are three comparators namely (C_{12} , C_{13} and C_{23}) and each comparator will check any mismatch occurred. When a mismatch happens, an error signal is activated. For an example, if there is a mismatch between outputs II and III is detected, TE_{13} and TE_{12} are activated. In order to detect permanent faults, the proposed voter provides three input signals (named Pr_{12} , Pr_{13} , and Pr_{23}), which are derived by the ScTMR controller. During the normal state and during recovery state, these three signals are deactivated ($Pr_{12} = Pr_{13} = Pr_{23} = 0$)[10]. output selector circuit uses E_{12} and E_{13} signals as the inputs of logical AND gate as the select line of the 2x1 multiplexer. If the module II, or module III is faulty, then the output of module I is selected as the error-free output. If module I is faulty, then Output II will be selected as the error-free output [10].

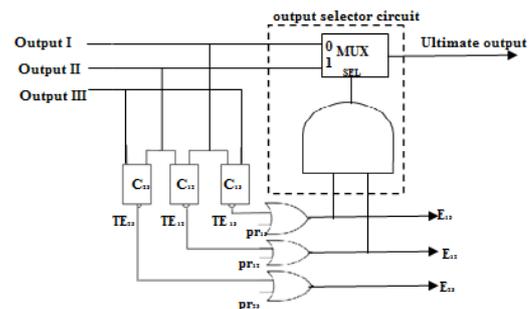


Figure 5: ScTMR Voter

2.2 SMERTMR Technique

Scan chain based multiple error recovery for a TMR system was introduced to overcome the drawbacks of ScTMR system. The SMERTMR technique has the ability to detect and remove latent faults in modules and to remove the multiple faults. There is one more mode of operation i.e. comparison mode in SMERTMR apart from the SCTMR. In the comparison mode of operation, bit by bit comparison is done to find the latent faults[10].

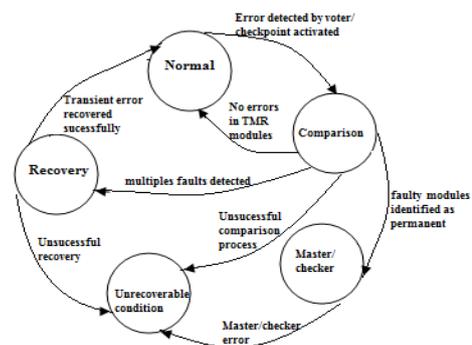


Figure 6: SMERTMR State diagram

State diagram(fig .6) shows the modes of operation. Initially the system will be in normal mode. Once an error is detected by the voter or when the checkpoint is activated, the system goes to comparison mode of operation. Comparison mode is activated by applying checkpoint signal to remove the latent faults. In comparison mode, bit-by-bit comparison is done to determine the faulty module and the type of error. If no error is found, then system goes to normal mode or else the system goes to recovery mode. Any failure during the operation will end with unrecoverable condition. During recovery process, the state of the fault free module is copied to the faulty module. If it detects a permanent fault, the system enters the Master or checker mode. In the M/C mode, any fault in the master or the checker modules results in an unrecoverable condition [10].The voter and the block diagram of SMERTMR are similar to SCTMR.

3. RPR Technique to Recover Multiple Errors

RPR is a technique similar to TMR technique where two reduced precision modules are used out of three modules. When the fully précised module fails to give the correct output then the reduced precision output is taken. This is shown in the fig.g.If the difference between fully précised output value and reduced précised value is greater than the threshold value, the reduced precision output is taken. If the difference is less than the threshold, then fully précised value is taken. The decision blocks and outputs can be triplicate as well to avoid single points of failure in those modules [1].

Fig 7 shows a block diagram of an n-bit FIR filter protected with RPR. The figure shows that the inputs to the filter are triplicated, as with TMR, and that the second and third replicas of the circuit are implemented with RP (k-bit, where $k < n$) FIR filters [1].

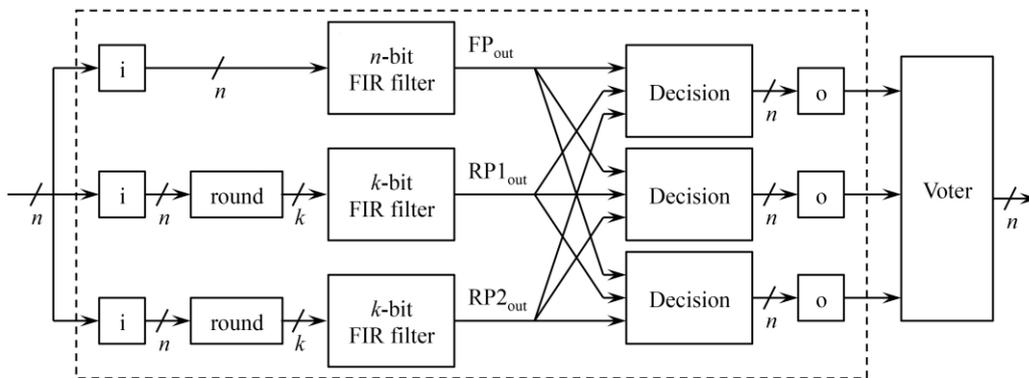


Figure 7: Block diagram of RPR

The main advantage of RPR Technique is that the area overhead is less than the other previous techniques.The modes of operation and the voter of the RPR is same as the SMERTMR System.

permanent fault, then module2 is discarded.if module3 is also faulty, and then the system will be halted.

4. Simulation Results

The proposed RPR Technique is designed using VHDL on Xilinx ISE simulator and compared with SCTMR & SMERTMR. Power consumption is calculated using X-power analyser.

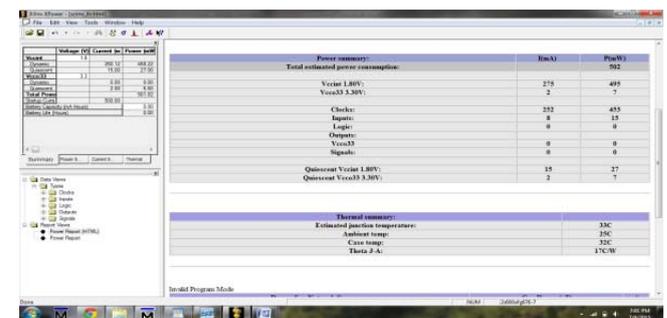


Figure 9: Power rating of ScTMR

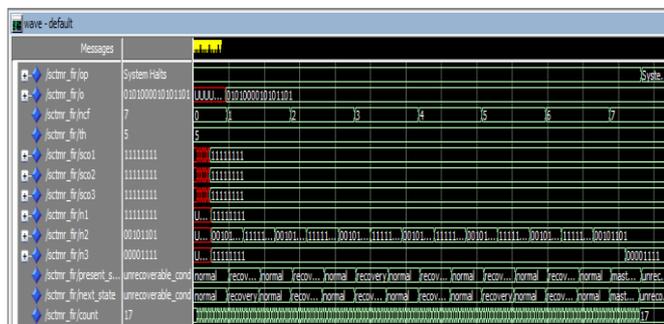


Figure 8: Simulation result of ScTMR

Here the modules are taken as FIR filter.simulatin results shows the case when two modules are faulty. If module2 is faulty then the module2 output is recovered. The fault free output is copied to the faulty module. However, if module2 is

Fig 9 shows the power analysis results of scan chain TMR.power is calculated in mill watt (mW) and power consumed by scan chain TMR is 502mW.

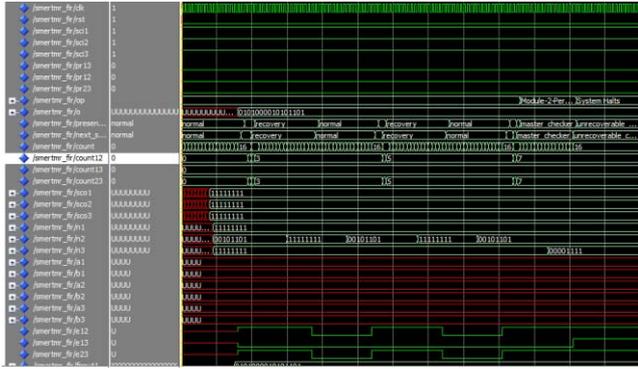


Figure 10: Simulation result of SMERTMR

Fig 10 shows the simulation result of scan chain multiple error recover in TMR technique used in FIR filter. During comparison mode, multiple faults can be detected and latent faults can be found out.

Power summary:	I(mA)	P(mW)
Total estimated power consumption:		483
Vccint 1.80V:	264	476
Vcco33 3.30V:	2	7
Clocks:	241	434
Inputs:	8	15
Logic:	0	0
Outputs:		
Vcco33	0	0
Signals:	0	0
Quiescent Vccint 1.80V:	15	27
Quiescent Vcco33 3.30V:	2	7

Figure 11: Power rating of SMERTMR

Fig 11 shows the power analysis results of scan chain multiple error recovery in TMR. Power is calculated in mill watt (mW) and power consumed by SMERTMR is 483mW.

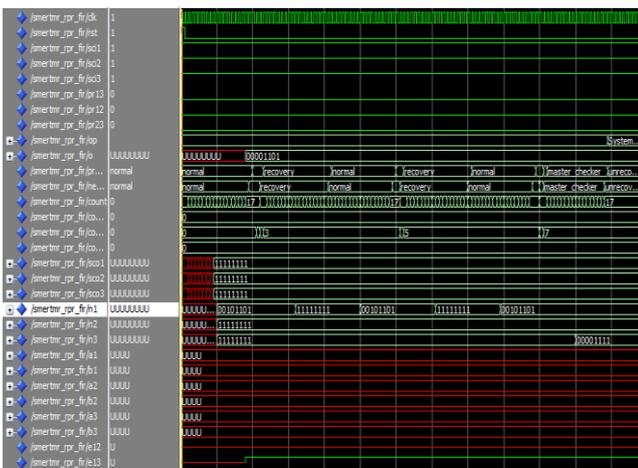


Figure 12: Simulation result of multiple error recovery using RPR

Fig.12 shows the simulation result of multiple error recovery in TMR using RPR technique used in FIR filter. All the

modes of operation is same as Scan chain multiple error recovery in TMR.

Power summary:	I(mA)	P(mW)
Total estimated power consumption:		420
Vccint 1.80V:	230	414
Vcco33 3.30V:	2	7
Clocks:	206	372
Inputs:	8	15
Logic:	0	0
Outputs:		
Vcco33	0	0
Signals:	0	0
Quiescent Vccint 1.80V:	15	27
Quiescent Vcco33 3.30V:	2	7

Figure 13: Power rating of multiple error recovery using RPR

Fig.13 shows the power analysis results of multiple error recovery in TMR using RPR Technique. Power is calculated in mill watt (mW) and power consumed by multiple error recovery in TMR using RPR Technique is 420mW.

5. Inferences

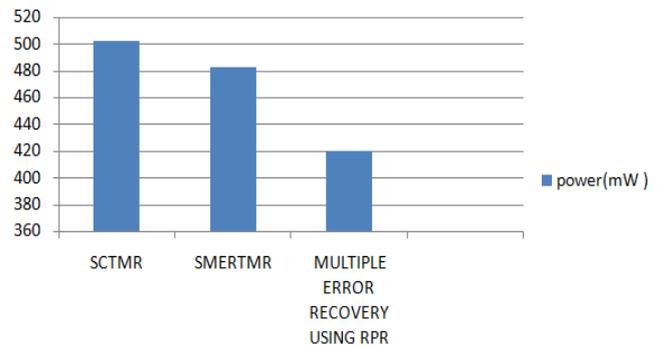


Figure 14: Power Consumption Comparison

Power is calculated in milli Watt (mW) and the comparison result in shown in fig.14. The power consumed and the area used in each of the system are compared and shown in the table.1. given below.

Table 1: Comparison Results

System used	Power consumption	Area (Gate count)
SCTMR	502mW	19,483
SMERTMR	483mW	20,356
RPR	420mW	14,030

The result shows that the proposed technique has greatly reduced in terms of area and power as compared to SCTMR & SMERTMR. The main advantage of this technique is that it can detect multiple faults with great minimization in terms of area and delay.

6. Conclusion

The proposed RPR technique is used to protect the Transposed FIR Filter. The Fully précised and other two reduced précised output is compared and the output is taken. The performance is compared the existing systems and found that the new proposed system greatly reduces the power and the area. The power is reduced from 483mW to

420mW. The main drawback i.e area and power of all other systems is reduced greatly.

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Author Profile



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