Designing the Efficient System with Matrix Converter to Replace Back to Back Converter in DFIG System with ISVM

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Abstract: This paper proposes the Doubly Fed Induction Generator (DFIG) System with Matrix Converter and DFIG system with Back to Back Converter. Matrix Converter is very efficient one in DFIG system than Back to Back Converter in Design constraints, Power quality, losses, and controlling. Both converters are controlled by Indirect Space Vector Modulation (ISVM) technique. LCL filters are designed for both converters. All results are simulated on MATLAB/SIMULINK software. Performance of the system is analysed by FFT window.

Keywords: Doubly Fed Induction Generator (DFIG), Matrix Converter (MC), Back to Back (B TB), Indirect Space-Vector Modulation (ISVM), LCL filters, Voltage source Inverter (VSI), Space Vector Modulation (SVM), Grid Side Converter (GSC), Rotor Side Converter (RSC).

1. Introduction

A Doubly fed induction generator (DFIG) is also called as wound rotor induction generator. “Doubly-Fed” means both the stator and rotor of an induction machine with a wound rotor is connected to electrical sources. This is popular for generation applications now-a-days with limited variable speed range. DFIG can produce or consume power from both stator and rotor of the machine depending upon the speed of the shaft.

Depending upon the value of a slip s, the operation of a DFIG can be classified in three different modes [1].

- \( w_r < w_s \ldots s > 0 \ldots \) Sub-synchronous mode.
- \( w_r > w_s \ldots s < 0 \ldots \) Super-synchronous mode.
- \( w_r = w_s \ldots s = 0 \ldots \) Synchronous mode.

LCL filters are designed for both converters both sides (i.e. rotor side and grid side) because both converters works in both directions. When the wind speed is low then the rotor receives power from grid and when the wind speed is high then the rotor will supply power to the grid. LCL filters can reduce the harmonics which are produced by converter [4-5]. Filters are connected in delta shape in both sides parameters of the delta connected filters are shown in bellow table.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>( L_1 )</th>
<th>( L_2 )</th>
<th>( R )</th>
<th>( C )</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rotor Side</td>
<td>0.73e-05</td>
<td>0.57</td>
<td>300e-06</td>
<td></td>
</tr>
<tr>
<td>Grid Side</td>
<td>5e-4</td>
<td>0.57</td>
<td>0.5F</td>
<td></td>
</tr>
</tbody>
</table>

The basic LCL filter circuit is shown in below Figure 2.

2. Converter Topologies

2.1 Back to Back Converter

Back to back converter is a two stage of converter, which converts AC-DC-AC. The AC-DC conversion is rectifier stage this converter is called as grid side converter (GSC) and the DC-AC conversion is inverter stage this converter is rotor side converter (RSC). Both converters are connected “back to back” with a DC link capacitor between them. DC link capacitor puts voltage at constant for efficient inversion. RSC controls the torque or speed of the DFIG and also...
controls the power factor of the stator terminals but the GSC only puts DC link voltage at constant.

![Back to Back Converter Diagram](image1)

**Figure 3: DFIG System with BTB**

GSC works at grid frequency but RSC works at different frequencies according slip speed variations. Back to back converter converts variable voltage, variable frequency into constant voltage, constant frequency or constant voltage, constant frequency into variable voltage, variable frequency in “back to back” directions [6].

### 2.2 Matrix Converter

Matrix converter consists of nine bidirectional solid-state switches. Bi-directional solid-state switch is shown in bellow Fig.

![Bi-directional Switch](image2)

**Figure 4: Bi-directional switch**

Matrix converter is an AC-AC converter. Which converts variable voltage, variable frequency into constant voltage, constant frequency or constant voltage, constant frequency into variable voltage, variable frequency. MC can control the direction of the current independently; conduction losses are lesser than back to back converter and also controls the power factor effectively [7-8].

Matrix converter doesn’t consist of DC link capacitor so that the cost of converter also reduced. Matrix converter technology is a newly lodged technology in wind energy generation it can convert AC-AC or AC-DC also. MC has three important topologies [9]. They are;

1. It can vary the input AC waveform so it simply called as AC controller topology.
2. It can be used if the output frequency is much lesser than the input source frequency this is cyclo-converter topology.
3. Finally it is matrix converter topology it is most robust without any limits on the output frequency and amplitude.

### 3. ISVM Control Technique

Both converters are controlled by indirect space-vector modulation (ISVM) scheme. It is similar to pulse width modulation but it is based on two-phase representation of three-phase quantities through transformation as follows.

$$\begin{align*}
\theta &= \frac{z + \alpha + \omega t}{2} \\
\alpha &= \frac{z + \omega t}{2}
\end{align*}$$

Switching states are produced by adopting conventional VSI topology and SVM concept. If we draw the matrix converter equivalent circuit, it consists of current source rectifier and voltage source inverter connected through virtual DC-link.

ISVM separates the control of the input current and output voltage. Nine switching pulses are produced by multiplying the two switching functions i.e. product or rectifier and inverter switching functions. Rectifier switching function and inverter switching functions are produced by SVM technique then two results will be combined [10].

$$\begin{align*}
\begin{bmatrix} S_{11} & S_{12} & S_{13} \\ S_{12} & S_{11} & S_{13} \\ S_{13} & S_{13} & S_{11} \end{bmatrix} &= \begin{bmatrix} S_1 & S_2 & S_3 \\ S_2 & S_1 & S_3 \\ S_3 & S_3 & S_1 \end{bmatrix} \times \begin{bmatrix} I_{d1} & I_{d2} & I_{d3} \\ I_{d2} & I_{d1} & I_{d3} \\ I_{d3} & I_{d3} & I_{d1} \end{bmatrix} \\
\begin{bmatrix} V_A \\ V_B \\ V_C \end{bmatrix} &= \begin{bmatrix} S_1 & S_2 & S_3 \\ S_2 & S_1 & S_3 \\ S_3 & S_3 & S_1 \end{bmatrix} \times \begin{bmatrix} V_d \\ V_a \\ V_c \end{bmatrix}
\end{align*}$$

#### 3.1 Rectifier Switching Functions

Mathematical representation of rectifier switching pattern in matrix form is [11]:

$$\begin{align*}
\begin{bmatrix} S_R \\ S_R \end{bmatrix} &= \begin{bmatrix} S_{R11} & S_{R12} & S_{R13} \\ S_{R21} & S_{R22} & S_{R23} \end{bmatrix} \times \begin{bmatrix} I_{d1} \\ I_{d2} \end{bmatrix} \\
\begin{bmatrix} I_s \\ I_s \end{bmatrix} &= \begin{bmatrix} S_{R11} & S_{R21} \\ S_{R12} & S_{R22} \end{bmatrix} \times \begin{bmatrix} I_{dc} \end{bmatrix}
\end{align*}$$
### Table 2: Rectifier Switching Function

<table>
<thead>
<tr>
<th>Vectors</th>
<th>( S_{R11} )</th>
<th>( S_{R12} )</th>
<th>( S_{R13} )</th>
<th>( S_{R21} )</th>
<th>( S_{R22} )</th>
<th>( S_{R23} )</th>
<th>( I_a )</th>
<th>( I_b )</th>
<th>( I_c )</th>
<th>( I_{off} )</th>
<th>( \delta )</th>
<th>( V_{DC} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( I_1 )</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>( I_{DC} )</td>
<td>0</td>
<td>-( I_{DC} )</td>
<td>( V_{DC} )</td>
<td>30</td>
<td>-( V_a )</td>
</tr>
<tr>
<td>( I_2 )</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>( I_{DC} )</td>
<td>-( I_{DC} )</td>
<td>( V_{DC} )</td>
<td>90</td>
<td>( V_{bc} )</td>
</tr>
<tr>
<td>( I_3 )</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>-( I_{DC} )</td>
<td>( I_{DC} )</td>
<td>0</td>
<td>( V_{DC} )</td>
<td>150</td>
</tr>
<tr>
<td>( I_4 )</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>-( I_{DC} )</td>
<td>0</td>
<td>( I_{DC} )</td>
<td>-( I_{DC} )</td>
<td>( V_{DC} )</td>
<td>-150</td>
</tr>
<tr>
<td>( I_5 )</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>-( I_{DC} )</td>
<td>( I_{DC} )</td>
<td>0</td>
<td>( V_{DC} )</td>
<td>-90</td>
</tr>
<tr>
<td>( I_6 )</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>( I_{DC} )</td>
<td>-( I_{DC} )</td>
<td>0</td>
<td>( V_{DC} )</td>
<td>-30</td>
</tr>
<tr>
<td>( I_7 )</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>( I_8 )</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>- 0</td>
</tr>
</tbody>
</table>

3.2 Inverter Switching Functions

Mathematical representation of rectifier switching pattern in matrix form is [11]:

\[
S_I = \begin{bmatrix} S_{I_{11}} & S_{I_{12}} & S_{I_{13}} \\
S_{I_{21}} & S_{I_{22}} & S_{I_{23}} \end{bmatrix}
\]

### Table 3: Inverter Switching Functions

| Vectors | \( S_{I11} \) | \( S_{I12} \) | \( S_{I13} \) | \( S_{I21} \) | \( S_{I22} \) | \( S_{I23} \) | \( v_A \) | \( v_B \) | \( v_C \) | \( v_{AB} \) | \( v_{BC} \) | \( v_{CA} \) | \( v_{Dc} \) | \( V_D \) | \( \delta \) | \( I_{DC} \) |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| \( V_1 \) | 1 | 0 | 0 | 1 | 0 | 1 | \( V_D \) | \( V_C \) | \( V_D \) | 0 | -\( V_{DC} \) | \( \sqrt{2}V_{DC} \) | 30 | \( I_A \) |
| \( V_2 \) | 1 | 0 | 1 | 0 | 0 | 1 | \( V_D \) | \( V_D \) | \( V_C \) | 0 | \( V_{DC} \) | \( \sqrt{2}V_{DC} \) | 90 | -\( I_C \) |
| \( V_3 \) | 0 | 1 | 1 | 0 | 0 | 1 | \( V_C \) | \( V_D \) | \( V_D \) | 0 | -\( V_{DC} \) | \( V_{DC} \) | 150 | \( I_B \) |
| \( V_4 \) | 0 | 1 | 1 | 0 | 1 | 0 | \( V_D \) | \( V_D \) | \( V_D \) | 0 | \( V_{DC} \) | \( V_{DC} \) | -150 | -\( I_A \) |
| \( V_5 \) | 0 | 1 | 1 | 1 | 0 | 1 | \( V_C \) | \( V_D \) | \( V_D \) | 0 | \( V_{DC} \) | \( V_{DC} \) | -90 | \( I_C \) |
| \( V_6 \) | 1 | 0 | 0 | 1 | 1 | 0 | \( V_C \) | \( V_D \) | \( V_D \) | 0 | \( V_{DC} \) | \( V_{DC} \) | -30 | -\( I_B \) |
| \( V_7 \) | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| \( V_8 \) | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

4. MATLAB/SIMULINK Circuits

4.1 DFIG System with Back to Back Converter
4.2 DFIG System with Matrix Converter

![Circuit Diagram of DFIG System with MC](image)

Figure 9: Circuit Diagram of DFIG System with MC

5. Simulation Results

5.1 DFIG System with Back to Back Converter

![Grid Side Voltage (Vabcg) Graph](image)

![Grid Side Current (Iabcg) Graph](image)

![Grid Active Power (Pg) Graph](image)
Figure 10: Grid side output waveforms

![Waveform Image]

Figure 11: FFT window of grid voltage & grid current

![FFT Image]
5.2 DFIG System with Matrix Converter

Figure 12: Rotor side output waveforms

Figure 13: BTB Converter Losses
Figure 14: Grid side output waveforms

Figure 15: FFT window of grid voltage & grid current

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6. Comparison of Both Converters

As we are comparing the both converters from the above results both are differentiated as shown in bellow table.

<table>
<thead>
<tr>
<th>System Parameters</th>
<th>BTB</th>
<th>MC</th>
</tr>
</thead>
<tbody>
<tr>
<td>No. of switches</td>
<td>12</td>
<td>9 (back to back)</td>
</tr>
<tr>
<td>Losses</td>
<td>0-800 (KW)</td>
<td>0-58 (W)</td>
</tr>
<tr>
<td>DC-Link Capacitor</td>
<td>12µF</td>
<td>No</td>
</tr>
<tr>
<td>THD (%)</td>
<td>1.81%</td>
<td>0.08%</td>
</tr>
<tr>
<td>Voltage Mag. at fundamental freq.</td>
<td>0.8794 (pu)</td>
<td>0.9025 (pu)</td>
</tr>
<tr>
<td>Current Mag. at fundamental freq.</td>
<td>1.156 (pu)</td>
<td>1.165 (pu)</td>
</tr>
<tr>
<td>Base Power</td>
<td>9 (MW)</td>
<td>9 (MW)</td>
</tr>
<tr>
<td>Total output Power</td>
<td>13-14.4 (MW)</td>
<td>14.4 (MW)</td>
</tr>
<tr>
<td>Conversion</td>
<td>AC-DC-AC (2-stages)</td>
<td>AC-AC (Single-stage)</td>
</tr>
</tbody>
</table>

- MC provides low-distortion sinusoidal input and output waveforms, bi-directional power flow, and controllable input power factor [12].
- The dc-link capacitor in BTB is heavy and bulky, increases the cost, and reduces the overall lifetime of the system [13].
- The main advantage of the MC is in its compact design which makes it suitable for applications where size and weight matter, such as in aerospace applications.

7. Conclusions

From the comparison of both converters we can observe that matrix converter is efficient one to design DFIG system for variable wind speeds. In order to track maximum power, MC adjusts the induction generator terminal frequency, and thus the turbine shaft speed. MC adjusts the reactive power...
transfer at the grid interface to achieve voltage regulation or power factor correction. MC is highly controllable and allows independent control of the output voltage magnitude, frequency, and phase angle, as well as the input power factor. Compared with the DC-link AC/AC converter systems, an advantage of the MC is elimination of the DC-link, including bulky capacitors. The MC structure is inherently capable of a four-quadrant operation. Finally this paper concludes that BTB has high converter losses, lesser magnitudes of voltage and current’s, higher THD% and lesser total output power than MC so that this paper proposes DFIG system with MC with efficient results.

References