

# Development of RIE Processes for the Etching of Single Crystal Silicon, Silicon Dioxide

Rekha Chaudhary<sup>1</sup>, Dhirender Kumar<sup>2</sup>, Supriyo Das<sup>3</sup>, B. D. Pant<sup>4</sup>

<sup>1</sup>M.Tech, S.G.V.U, Jaipur, India

<sup>2,3</sup>Technical Assistant, CSIR-CEERI, Pilani, India

<sup>3</sup>Cheif Scientist, CSIR-CEERI, Pilani, India

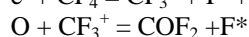
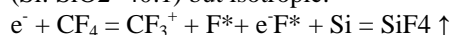
**Abstract:** Etching of micro-structures in a single crystal Si, SiO<sub>2</sub> has been obtained using reactive ion etching with SF<sub>6</sub>/O<sub>2</sub> and CF<sub>4</sub>/O<sub>2</sub> gas mixtures respectively. The variation in etch rate of Si and SiO<sub>2</sub> has been observed by varying gas composition, reactants flow rate, pressure and duration of plasma process. The reactive ion etching is normally carried out using RF (Radio frequency) plasma. In the present work, we have utilized LF (Low frequency) for the RIE (Reactive ion etching) processes. High etch rate of Si of 0.344μm/min has been achieved with SF<sub>6</sub>/O<sub>2</sub> plasma and etch rates of SiO<sub>2</sub> are 0.0316μm/min achieved using CF<sub>4</sub>/O<sub>2</sub> at 40 KHz. The results show that O<sub>2</sub> concentration and pressure has strong effect on etch rates. Also the variation in etch rate is influenced by variation in power. The etching processes developed in this work are aimed for surface micro-machining of silicon for the fabrication of MEMS (micro electro mechanical system) devices.

**Keywords:** reactive ion etching, Low frequency.

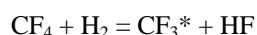
## 1. Introduction

Si is a promising material for development of high temperature solid-state electronics and having properties like larger band gap, smaller leakage current, strong, brittle and immune to noise. Silicon has also been used to fabricate MEMS micro devices due to its excellent mechanical properties. In order to create micro structures in Si, we need to develop Si etching processes. Silicon dioxide is a successful material because of its stable oxide. Various forms of Silicon oxide are used in micromachining due to excellent electrical and thermal insulating properties. Reactive ion etching is a combination of physical and chemical processes that leads to formation of micro-structures like micro-needles using surface micro-machining. Isotropic profile is most common in dry etching when halogen etchants are used. Si is etched by halogens at room temperature and at milli-torr pressure because of strong halogen bond (550KJ/mol). Fluorine plasmas are milder and resist selectivity is better. Fluorine based gases are the most popular reactive gases for the Etching of Si. The free radical F\* spontaneously react with Si to produce a volatile product. But, SF<sub>6</sub> based processes use 10-40% of added O<sub>2</sub>. Oxygen reacts with SF<sub>6</sub> and keeps fluorine concentration high by preventing fluorine recombination with the fragments. SF<sub>6</sub> etches a few microns of Si but isotropic under-cutting is sometimes desirable and even necessary in order to fabricate free standing beams.

Silicon etching can be done by fluorine based plasma and chlorine based plasma. In fluorine based plasma CF<sub>4</sub>, SF<sub>6</sub>, C<sub>2</sub>F<sub>4</sub>, C<sub>3</sub>F<sub>8</sub> etc. any gas can be used. All the gases contain Fluorine free radicals that are the most important species for silicon etching. The reaction between silicon and F\* is spontaneous. The profile that is achieved is very selective (Si: SiO<sub>2</sub>=40:1) but isotropic.



The extra electron that is obtained maintain the charge neutrality and oxygen is added to increase the etch rate. In chlorine based plasma, mixture of C<sub>2</sub>F<sub>6</sub> and Cl<sub>2</sub> is used. Where C<sub>2</sub>F<sub>6</sub> serves two functions, one to etch Si and SiO<sub>2</sub> and other is to create a passivation layer in order to make the profile anisotropic. For undoped Si, the reaction in chlorine based plasma is not spontaneous but for doped Si, the reactions are spontaneous. For the etching of SiO<sub>2</sub>, hydrogen is used with CF<sub>4</sub> instead of oxygen. HF etches silicon dioxide. Also hydrogen suppresses the formation of fluorine free radical and very good selectivity is achieved (SiO<sub>2</sub>: Si=35:1).



The results of RIE of single crystal Si and Silicon Dioxide are obtained using tetra-30. It is a parallel plate reactor having software PRS (plasma reaktar steuerung) for the control of plasma machine. In this, a recipe is created having parameters like pumping down time, gas supply, plasma process, flushing and venting period. This system gives an actual pressure of 172.29mbar and can handle maximum flow rate of 2000sccm, 52sccm of O<sub>2</sub> and SF<sub>6</sub> respectively. MW generator operates on 13.56 MHz and LF operates at 40 KHz. But here etching is done at 40 KHz. In the plasma chamber, both electrodes are cooled by liquid nitrogen before the program runs.

## 2. Experimental SETUP

TETRA-30 is a photo resist stripper that is used to remove the photo resist from the substrate surface. The photo resist can be removed by burning it in oxygen plasma. But it can also be used for the purpose of reactive ion etching. The reactor consists of two parallel plates' electrodes, where the wafers are kept on powered electrodes. There are two power supplies, one is LF and other is magnetron. There is vacuum system connected at one side in order to create vacuum and

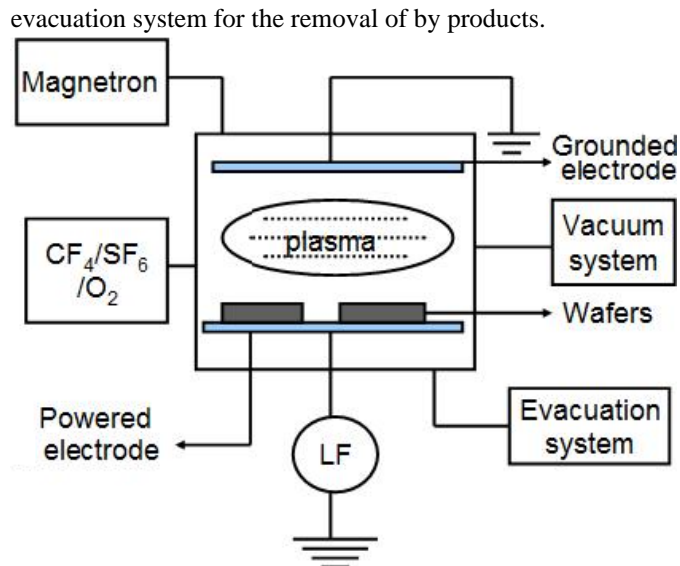


Figure 1: Schematic of TETRA-30

TETRA-30 operates in two different modes. In automatic mode, a program consists of different processes is created through process parameters. A program can be built by sub-programs and consists of upto 10 sub-programs. The program parameters can be saved. A sub-program consists of:-

- Pumping down
- Gas supply
- Plasma process
- Flushing period
- Venting period

Pumping down is used to create vacuum in the chamber and the maximum pumping down time is set. If this time exceeds, an error message is generated, entry in alarm list appears and display field of the switch is red. As soon as pumping down starts, display switch turns green. Gas supply input is made in sccm. Different MFC (mass flow controller) are connected to the system. Plasma process duration defines the duration for which plasma is created in the chamber. During this time dissociation, ionization, excitation all the processes take place. Plasma process time in hour, minutes, seconds is set. It also depends upon the material to be etched. For some material plasma process duration is kept in hours but for some it is kept even in minutes. The set power and maximum absolute deviation is entered. If the value exceeds, entry in alarm list appears. The minimum and maximum temperature must be entered. The process starts if the minimum temperature is reached. Flushing period is entered and during this time all the gases are flush out. Flushing time is set in minutes. Venting is done in order to set the chamber pressure equals to atmospheric pressure so that the door of chamber can easily be opened. Venting period should be chosen such that atmospheric pressure is reached in vessel and display field of switch turns yellow.

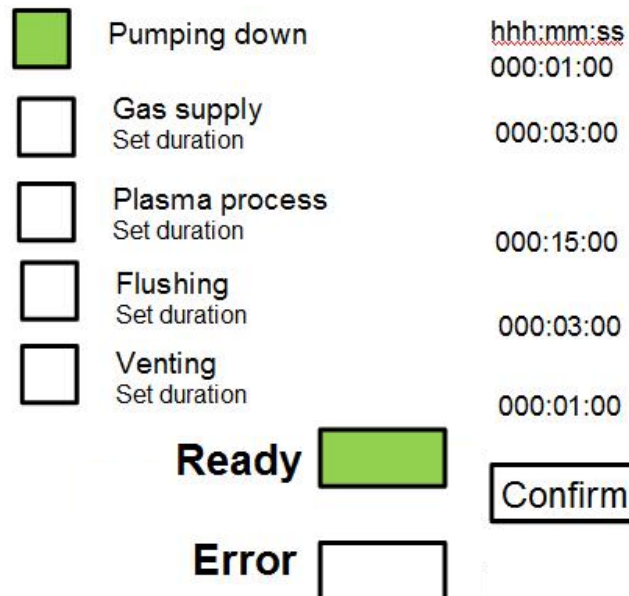


Figure 2: Sub program run in TETRA-30

### 2.1 Development of RIE processes for Si

<100> oriented, n type Si wafers were used as substrate and piranha cleaning was done. Positive photo-resist S-1818 was deposited using spin on method at 2000rpm, soft baking at 90°C. Photo resist of 2.8 μm thickness was deposited on wafer. Then the pattern was defined by UV exposure. The pattern was recovered using photo resist developer CD-26. After development, the wafer was hard baked in oven at 120°C. Then wafer was placed in tetra-30. Reactive ion etching at LF was done using suitable recipe.

The gases used for this purpose were SF<sub>6</sub> and O<sub>2</sub>. Dry etching has been performed in TETRA-30. The etch rates were measured using Surface Profiler and Zeta. The etch rates were found to be dependent upon the power and pressure. Etch rates at two different pressure .15mbar, 0.1mbar were found 0.344μ/min and 0.304μ/min respectively.

### 2.2 Development of RIE processes for SiO<sub>2</sub>

<111> oriented p-type wafers were used as substrate and piranha cleaning was done. PECVD oxide of .8micron was deposited. Positive photo-resist S-1818 of 2.8 micron thickness was deposited using spin on method at 2000 rpm rate. Soft baking at 90°C and a pattern was defined by UV exposure. Then pattern was recovered using photo resist developer CD-26. After development, the wafer was hard baked in oven at 120°C. Then wafer was placed in tetra-30. Reactive ion etching at LF was done using suitable recipe.

CF<sub>4</sub>/O<sub>2</sub> gas mixture was used for this purpose. Different etch rates of 0.3.16μm/min at 0.1mbar and 0.23μm/min at 0.3 mbar were obtained. The etch rates were found dependent on pressure in side the chamber and power.

## 3. Results and Discussion

Different recipes were developed for different wafers. Nine wafers of silicon and nine wafers of silicon dioxide were

taken. On three wafers, same recipe was run. Then the recipe was changed. Then the modified recipe was run on other three wafers. In this way the different etch rates were obtained by changing the power and pressure. For first three samples, when the pressure is 0.5mbar then at different power i.e. 400W, 600W, 800W, different etch rates were obtained. Similarly same process was carried out for 0.1mbar and 0.15mbar pressure. But for silicon dioxide, for first three samples when the pressure is 0.1mbar and next three samples when pressure is 0.15mbar, the power is kept 400W, 500W, 600W but for last three samples when pressure is 0.3mbar power values are kept 600W, 700W and 800W. By varying the power, different etch rates were obtained. Etch rates also depends upon the duration of plasma process.

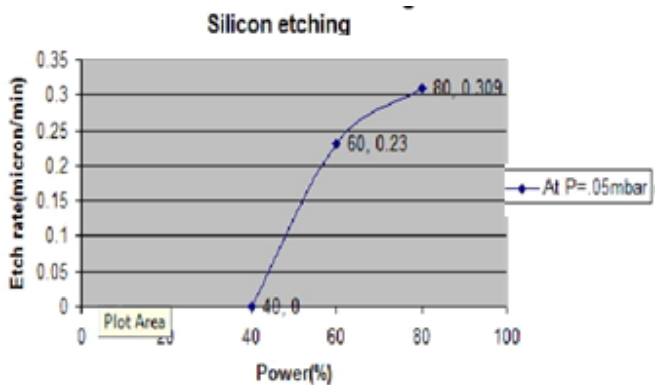


Figure 3 Etch rates of samples at 0.5mbar

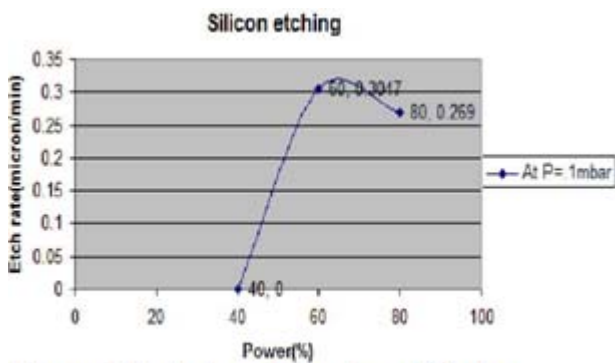


Figure 4 Etch rates of samples at 0.1mbar

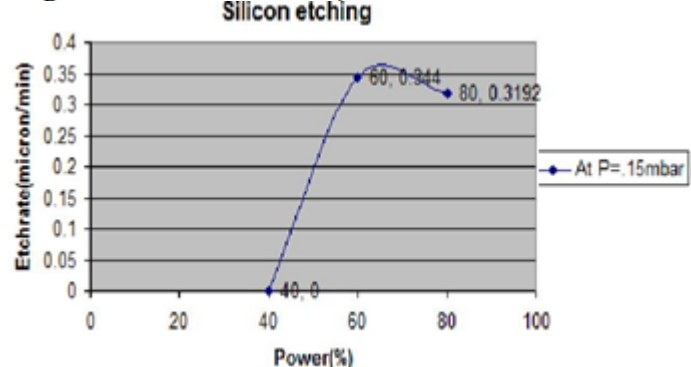


Figure 5: Etch rates of samples at 0.15mbar

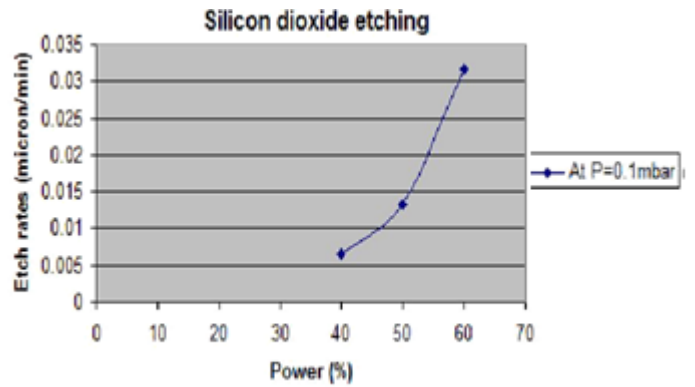


Figure 6 Etch rates of samples at 0.1mbar

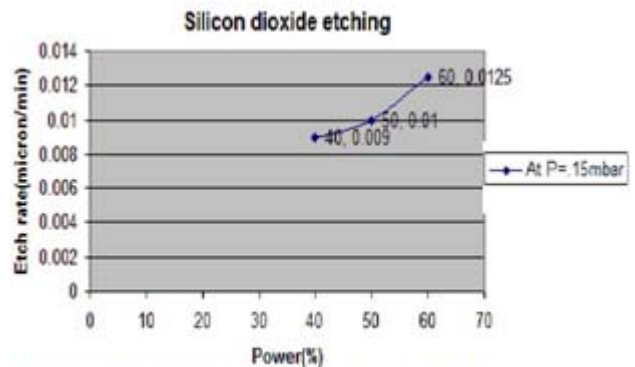


Figure 7 Etch rates of samples at 0.15mbar

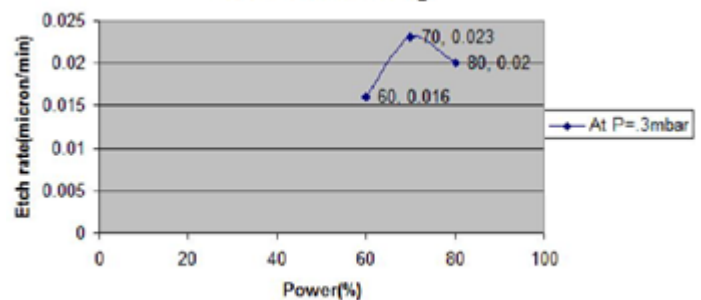


Figure 8: Etch rates of samples at 0.3mbar

For these experiments, etch rates were evaluated as the function of pressure of the chamber and power. As power increases, etch rate increases but as pressure increases, etch rates decreases. For Si, when the pressure increases, the etch rate increases and achieves the maximum value of 0.344 $\mu$ m/min and after a certain pressure value i.e. 0.1mbar, etch rate decreases. For SiO<sub>2</sub>, etch rates increases initially and achieves the maximum value of 0.0316 $\mu$ m/min but after 0.3mbar pressure etch rates decreases. When power increases beyond 70%, the etch rate decreases because the pressure is goes on increasing simultaneously. For 40% of power no etch rates were observed independent of pressure values.

#### 4. Conclusion

For better etch rates, the pressure for Si should be kept 0.1 mbar and also power should be greater than 40%. For SiO<sub>2</sub>, the pressure should be kept 0.3mbar and then the power should be increased. The increase in power leads to increases in ionization that leads to more number of free radicals and more etching takes place. But increase in pressure, decreases the mean free path of radicals. So, the ions collide with each other rather than collision with the

surface. Ions crowding take place just above the surface that decreases the etch rate.

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