Analysis of Five Level Cascaded Multilevel Inverter Using Variable Frequency Multi Carrier Pulse Width Modulation Techniques

Shani M Vaidya¹, Kunal P Bhatt²

¹M.Tech 2nd year Student in Department of Electrical School of Engineering RK, University, Rajkot, Gujarat India.
² Professor, Head of Department of Electrical School of Engineering RK, University, Rajkot, Gujarat India

Abstract: Multilevel inverters are used in the many high power application, it has many advantages due to the controllability of the series connected switches. In this paper presents the different variable frequencymulti carrier modulation pulse width modulation techniques modeled in MATLAB Simulink. The analysis cascaded multilevel inverter using the different modulation technique schemes is done and finally the results are compared. The main objective of this work is to select the best control and switching topology for the multilevel inverter

Keywords: Multilevel inverter, Total harmonics distortion, Modulation techniques, MATLAB

1. Introduction

Power electronics based converters and controllers are used for different types of domestic, industrial application and many high voltage application. The main advantages of the multilevel inverter are the good power quality and reduction in the harmonics distortion and better high voltage stability. The multilevel inverter topologies classified into three different types that is diode clamped, capacitor clamped and cascaded multilevel inverter. In this paper high frequency modulation techniques like variable switching frequency based phase-shifted carrier modulation and level-shifted carrier modulation is present. The main aim of present this paper to minimize total harmonic distortion and increase the stability of the output voltage. Moreover these modulation techniques are compared with sub harmonic pulse width modulation techniques. In the Fig 1 shows the classification of the different modulation technique on the basis of the frequency.

![Figure 1: Classification of the modulation techniques](image1)

2. Variable Switching Frequency Pulse –Width Modulation

In a conventional constant frequency methods are the carrier wave of the same frequency is compared with reference signal but in a variable switching frequency two different frequencies are used as carrier wave and its compare with the references signal, it shown in the fig 2. In this proposed method, the carrier frequency of a five-level inverter is assigned to have a variable frequency of 2000 and 5000Hz as shown in the fig.

![Figure 2: Variable Frequency sine wave pulse width modulation](image2)

3. Phase Shifting Pulse-Width Modulation

In a multilevel inverter the number of carrier signal

$$m = (n - 1)$$

Where n=number of level of the inverter.

For 5 level inverter

$$m = (5 - 1) = 4.$$ 

For phase shifting multicarrier modulation all the triangular carrier wave is Phase shifted by 360° ÷ (m)

In the fig 3 shows the phase shifted multilevel modulation

![Figure 3: Phase shifting pulse width modulation](image3)
4. Level Shifting Pulse- Width Modulation

4.1 Phase disposition modulation techniques:
In this modulation techniques all the carrier wave of the same amplitude are in same phase. The fig 4 shows phase disposition techniques.

4.2 Phase opposite disposition modulation techniques
In this modulation techniques all the carrier wave of the same amplitude with reference of zero above and below are in opposite phase with each other, it’s shown in the fig 5.

4.3 Alternative Phase opposite disposition modulation techniques:
In this modulation technique all the carrier waves of the same amplitude are in opposite phase with respect to each other it is show in the fig 6.

5. Switching Frequency Optimal Pulse –Width Modulation
The switching frequency optimal pulse width modulation (SFOPWM) which triples harmonics voltage is added to each of the carrier waveform [1]. The method takes the instantaneous average of the maximum and minimum of the reference voltage and subtracts the value from reference voltage for obtain the modulation waveform [1].

\[ V_{offset} = \frac{\text{max}V + \text{min}V}{2} \]
\[ V_{sfo} = V - V_{offset} \]

In the fig 7 shows modulation waveform of the SFOPWM. The advantage of the method is the modulation index increase the 15% as compare to the convectional PWM methods, its helps to reduce the harmonics distortion.

6. Simulation
To verify the above modulation techniques a simulation model for single phase five level cascaded multilevel is implemented. The simulation parameters for a variable frequency 2KHz and 5KHz switching frequency each
voltage source has 53 volts dc voltage apply to the single phase load R=100.

Figure 8: Output voltage Waveform of Phase shifting Method

Figure 9: Output Voltage Waveform Of PD

Figure 10: Output Voltage Waveform Of POD

Figure 11: Output Voltage Waveform Of APOD

Figure 12: Output Voltage Waveform Of SFO-Phase Shifting

Figure 13: Output Voltage Waveform of SFO-PD
Figure 14: Output Voltage Waveform Of SFO-POD

Figure 15: Output Voltage Waveform Of SFO-APOD

Figure 16: THD Analysis Of Phase Shifting Method.

Figure 17: THD Analysis Of PD.

Figure 18: THD Analysis Of POD.

Figure 19: THD Analysis Of APOD

Figure 20: THD Analysis Of SFO Phase Shifting Method.

Figure 21: THD Analysis Of SFO PD.

Figure 22: THD Analysis Of SFO POD.
7. Result Analysis

<table>
<thead>
<tr>
<th>Modulation Technique</th>
<th>Output Voltage</th>
<th>% THD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Phase Shifting</td>
<td>105.4v</td>
<td>62.36%</td>
</tr>
<tr>
<td>Phase Disposition</td>
<td>105.5v</td>
<td>35.29%</td>
</tr>
<tr>
<td>Phase Opposite Disposition</td>
<td>105.5v</td>
<td>31.34%</td>
</tr>
<tr>
<td>Alternative Phase Disposition</td>
<td>105.5v</td>
<td>30.85%</td>
</tr>
<tr>
<td>SFO Phase Shifting</td>
<td>105.7v</td>
<td>29.74%</td>
</tr>
<tr>
<td>SFO Phase Disposition</td>
<td>105.7v</td>
<td>28.87%</td>
</tr>
<tr>
<td>SFO Phase Opposite Disposition</td>
<td>105.6v</td>
<td>26.74%</td>
</tr>
</tbody>
</table>

8. Conclusion

From this proposed work of multilevel sinusoidal pulse width modulation with variable carrier frequency for H-bridge five level inverter. The harmonics contents for alternative phase opposite disposition pulse width modulation technique contain lower harmonics than others. By increasing the level we can reduce harmonics and improve the efficiency of output voltage waveform.

References