Mitigation of Lower Order Harmonics in a Grid-Connected Single-Phase PV Inverter

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Abstract: In this paper, a simple single-phase grid-connected photovoltaic (PV) inverter topology consisting of a boost section, a lowvoltage single-phase inverter with an inductive filter, and a step-up transformer interfacing the grid is considered. Ideally, this topology will not inject any lower order harmonics into the grid due to high-frequency pulse width modulation operation. However, the nonideal factors in the system such as core saturation-induced distorted magnetizing current of the transformer and the dead time of the inverter, etc., contribute to a significant amount of lower order harmonics in the grid current. A novel design of inverter current control that mitigates lower order harmonics is presented in this paper. An adaptive harmonic compensation technique and its design are proposed for the lower order harmonic compensation. In addition, a proportional-resonant-integral (PRI) controller and its design are also proposed. This controller eliminates the dc component in the control system, which introduces even harmonics in the grid current in the topology considered. The dynamics of the system due to the interaction between the PRI controller and the adaptive compensation scheme is also analyzed. The complete design has been validated with experimental results and good agreement with theoretical analysis of the overall system is observed.

Keywords: Adaptive filters, harmonic distortion, inverters, solar energy, PRI, THD.

1.Introduction

Renewable sources of energy such as solar, wind, and geothermal have gained popularity due to the depletion of conventional energy sources. Hence, many distributed generation (DG) systems making use of the renewable energy sources are being designed and connected to a grid. The topology of the solar inverter system is simple. It consists of the following three power circuit stages:

- 1) A boost converter stage to perform maximum power point tracking (MPPT);
- 2) A low-voltage single-phase *H*-bridge inverter;
- 3) An inductive filter and a step-up transformer for interfacing with the grid.

The system will not have any lower order harmonics in the ideal case. However, the following factors result in lower order harmonics in the system: The distorted magnetizing current drawn by the transformer due to the nonlinearity in the B-H curve of the transformer core, the dead time introduced between switching of devices of the same leg [2]–[6], on-state voltage drops on the switches, and the distortion in the grid voltage itself.

There can be a dc injection into the transformer primary due to a number of factors. These can be the varying power reference from a fast MPPT block from which the ac current reference is generated, the offsets in the sensors, and A/D conversion block in the digital controller. This dc injection would result in even harmonics being drawn from the grid, again contributing to a lower power quality.

The advantage of the adaptive filter-based method is the inherent frequency adaptability which would result in same amount of harmonic compensation even when there are shifts in grid frequency. The implementation of adaptive filters is simple. Thus, in this paper, an adaptive filter-based method is proposed. This method estimates a particular harmonic in the grid current using a least-mean-square (LMS) adaptive filter and generates a harmonic voltage reference using a proportional controller. This voltage reference is added with appropriate polarity to the fundamental voltage reference to attenuate that particular harmonic.

This paper includes an analysis to design the value of the gain in the proportional controller to achieve an adequate level of harmonic compensation. The effect of this scheme on overall system dynamics is also analyzed. This method is simple for implementation and hence it can be implemented in a low-end digital controller.

2. Origin of Lower Order Harmonics and Fundamental Current Control

This section discusses the origin of the lower order harmonics in the system under consideration. The sources of these harmonics are not modeled as the method proposed to attenuate those works independent of the harmonic source.

A. Origin of Lower Order Harmonics

- 1) Odd Harmonics: The dominant causes for the lower order odd harmonics are the distorted magnetizing current drawn by the transformer, the inverter dead time, and the semiconductor device voltage drops. Other factors are the distortion in the grid voltage itself and the voltage ripple in the dc bus. The magnetizing current drawn by the transformer contains lower order harmonics due to the nonlinear characteristics of the B-H curve of the core.
- 2)*Even Harmonics:* The topology under consideration is very sensitive to the presence of dc offset in the inverter terminal voltage. The dc offset can enter from a number of factors such as varying power reference given by a fast MPPT block.

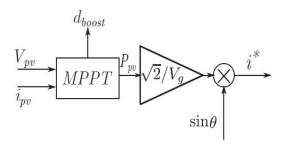


Figure 1: Generation of an inverter ac current reference from an MPPT block.

In Fig. 1, d_{bonst} is the duty ratio command given to the boost converter switch, v_{py} and t_{py} are the panel voltage and current respectively.

B. Fundamental Current Control

1) Introduction to the PRI Controller: Conventional stationary reference frame control consists of a PR controller to generate the inverter voltage reference. a modification to the PR controller is proposed, by adding an integral block, *GI* as indicated in Fig. 2. The modified control structure is termed as a PRI controller.

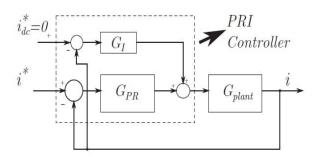


Figure 2: Block diagram of the fundamental current control with the PRI controller.

Here

$$G_1 = \frac{K_2}{S}$$
(1)

$$G_{PB}(\alpha) = K_p + \frac{K_p S}{S^{\alpha} + 0.5^{\alpha}}$$
(2)

The plant transfer function is modeled as

$$G_{\text{plant}}(s) = \frac{V_{dc}}{R_{co}GL_{c}}$$
(3)

This is because the inverter will have a gain of \mathbf{v}_{de} to the voltage reference generated by the controller and the impedance offered is given by $(\mathbf{R}_{c} + \mathbf{SL}_{c})$ in s-domain. *Rs* and *Ls* are the net resistance and inductance referred to the primary side of the transformer respectively.

2) Design of PRI Controller Parameters: The fundamental current corresponds to the power injected into the grid. The control objective is to achieve UPF operation of the inverter. First, a PR controller is designed for the system assuming that the integral block is absent, i.e., KI = 0. Design of a PR controller is done by considering a PI controller in place of the PR controller. Let

$$\mathbf{G}_{\mathbf{p}\mathbf{i}}(\mathbf{s}) = \mathbf{K}_{\mathbf{p}\mathbf{i}} \frac{\mathbf{s} + \mathbf{s}\mathbf{i}}{\mathbf{s}\mathbf{T}} \tag{4}$$

With the PI controller as the compensator block and without integral block, the forward transfer function will be

 $G_{\text{forwe}}(\mathbf{A}) = \left(K_{\mathbf{p}1} \frac{1+aT}{aT} \right) \frac{V_{de}}{K_{\mathbf{p}}+aL_{\mathbf{a}}}$ (5)

If the required bandwidth, then Kp1 can be chosen to be

$$K_{p1} = \frac{\alpha_{pW} R_p T}{V_{p1}} \tag{6}$$

Now, if the PI controller in (5) is written as

$$G_{p_1}(S) = K_{p_1} + \frac{K_{p_1}}{S}$$
 (7)

The closed-loop transfer function is given as

$$\mathbf{G}_{\mathbf{g}_1,\mathbf{P}\mathbf{R}\mathbf{I}} = \frac{\mathbf{I}(\mathbf{S})}{\mathbf{I}^*(\mathbf{S})} = \frac{\mathbf{U}_{\mathbf{g}_1\mathbf{R}\mathbf{n}}\mathbf{U}_{\mathbf{P}\mathbf{R}}}{\mathbf{I}^*\mathbf{U}_{\mathbf{g}_1\mathbf{R}\mathbf{n}}(\mathbf{U}_{\mathbf{P}\mathbf{R}}^*\mathbf{U}_{\mathbf{I}})}$$
(8)

Without the integral block, the closed-loop transfer function would be

$$\mathbf{G}_{el,PR} = \frac{\mathbf{G}_{Plant}\mathbf{G}_{PR}}{\mathbf{1} + \mathbf{G}_{Plant}\mathbf{G}_{PR}} \tag{9}$$

$$G_{\text{plant}} = \frac{M}{1+\epsilon T}$$
 (10)

Where M=Vdc/Rs. The numerators in both (8) and (9) are the same. Thus, the difference in their response is only due to the denominator terms in both. The denominator in (8) can be obtained as

$$\frac{\operatorname{den} \mathbf{P}^{\mathbf{R}} \mathbf{I} = \left[\frac{\operatorname{Te}^{4} + (\mathbf{i} + \mathrm{MK}_{p}) \mathbf{s}^{5} + (\omega_{0}^{2} \mathrm{T} + \mathrm{M}(\mathrm{K}_{r} + \mathrm{K}_{l})) \mathbf{s}^{2}}{\mathbf{s}(\mathbf{1} + \mathrm{s} \mathrm{T})(\mathbf{s}^{2} + \omega_{0}^{2})} + \frac{\omega_{0}^{2} ((\mathbf{1} + \mathrm{MK}_{p}) \mathbf{s} + \mathrm{MK}_{l} \omega_{0}^{2})}{\mathbf{s}(\mathbf{1} + \mathrm{s} \mathrm{T})(\mathbf{s}^{2} + \omega_{0}^{2})} \right]$$

$$(11)$$
Similarly, the denominator in (9) is given by
$$\operatorname{den} \mathbf{P} \mathbf{R} = \left[\frac{\operatorname{Te}^{2} + (\mathbf{i} + \mathrm{MK}_{p}) \mathbf{s}^{2} + (\omega_{0}^{2} \mathrm{T} + \mathrm{MK}_{p}) \mathbf{s}}{(\mathbf{1} + \mathrm{s} \mathrm{T})(\mathbf{s}^{2} + \omega_{0}^{2})} + \frac{(\mathrm{MK}_{p} + \mathrm{I})\omega_{0}^{2}}{(\mathbf{1} + \mathrm{s} \mathrm{T})(\mathbf{s}^{2} + \omega_{0}^{2})} \right]$$

$$(12)$$

3. Adaptive Harmonic Compensation

In this section, the concept of lower order harmonic compensation and the design of the adaptive harmonic compensation block using this adaptive filter are explained.

A. Review of the LMS Adaptive Filter

The adaptive harmonic compensation technique is based on the usage of an LMS adaptive filter to estimate a particular harmonic in the output current. This is then used to generate a counter voltage reference using a proportional controller to attenuate that particular harmonic.

B. Adaptive Harmonic Compensation

The LMS adaptive filter discussed previously can be used for selective harmonic compensation of any quantity, say grid current. To reduce a particular lower order harmonic (say ik) of grid current:

- 1) *ik* is estimated from the samples of grid current and phase locked loop (PLL) [38] unit vectors at that frequency;
- 2) A voltage reference is generated from the estimated value of *ik*;
- 3) Generated voltage reference is subtracted from the main controller voltage reference.

The Fig.3 shows the power circuit topology considered. This topology has been chosen due to the following advantages: The switches are all rated for low voltage which reduces the cost and lesser component count in the system improves the overall reliability.

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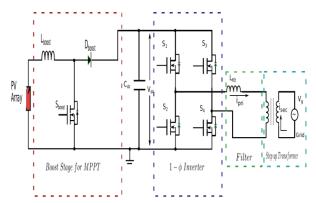


Figure 3: Power circuit topology of the 1 – phase PV system for a low-voltage inverter with 40V dc bus connected to 230V grid using a step-up transformer This topology will be a good choice for low-rated PV inverters of rating less than a kilowatt. The disadvantage would be the relatively larger size of the interface transformer compared to topologies with a high-frequency link transformer.

4. Simulation Results

4.1 Grid connected single-phase PV inverter before compensation

The grid connected single-phase PV inverter before compensation is shown in fig.4.

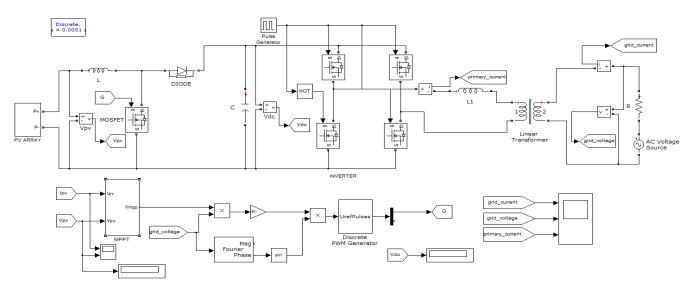


Figure 4: Grid connected single-phase PV inverter before compensation

The Fig.4. Consists of PV array, boost converter, singlephase inverter, an inductive filter& a step-up transformer for interfacing with the grid. The PV array gives the values are $v_{PU}=10.69v$, $u_{PU}=10.09A$, $v_{ed}=14.55v$. The boost converter boost up the voltage & current. The capacitor is used to the purpose of continuous current flowing. The inverter converts DC power to AC power. In the inverter each IGBT has resistance of 0.1Ω .the transformer having nominal power of 150VA; operating frequency is 50HZ, L=500g⁻², C=6600g⁻², R=0.1\Omega, in ac voltage source, peak amplitude=230V.

4.2 Block Diagram OF PV Array

The block diagram of PV array is shown in fig.5

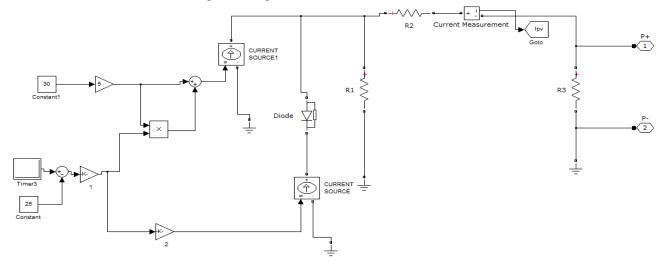


Figure 5: block diagram of PV array Volume 3 Issue 11, November 2014 <u>www.ijsr.net</u> Licensed Under Creative Commons Attribution CC BY In the PV array, $R_1 = 1\Omega$, $R_2 = 100\Omega$, $R_3 = 5\Omega$. For diode R=0.001 Ω , $V_f = 0.8V$, subber resistance $R_s = 500\Omega$, subber capacitance $C_s = 250e^{-\frac{6}{2}}$.

4.3 Block Diagram of MPPT Algorithm The block diagram of MPPT algorithm is shown in fig.6

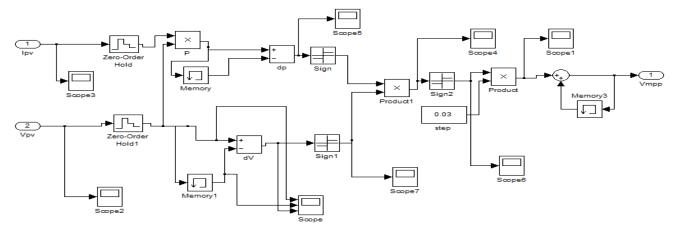


Figure 6: Block diagram of MPPT Algorithm

The Fig.6. Shows Block diagram of MPPT Algorithm. It gives the max. power to the circuit.

5. Grid Connected Single-Phase PV Inverter with PR Controller

The grid connected single-phase PV inverter with PR controller is shown in fig.7.

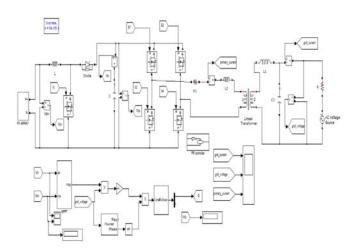


Figure 7: Grid connected single-phase PV inverter with PR controller

The Fig.7. Consists of PV array, boost converter, singlephase inverter, an inductive filter& a step-up transformer for interfacing with the grid. The PV array gives the values are $V_{PV}=29.62V$, $I_{PV}=9.90A$, $V_{de}=41.7v$. The boost converter boost up the voltage & current. The capacitor is used to the purpose of continuous current flowing. The inverter converts DC power to AC power. The transformer having nominal power of 150VA; operating frequency is 50HZ, $L=500e^{-2}$, $L_1=10e^{-2}$, $L_2=100e^{-6}$, $C=6600e^{-6}$, $C_1=100e^{-6}$, $R=0.1\Omega, R_1=90\Omega.$ In ac voltage source, peak amplitude=230V. it is also consists of PR controller.

5.1 Block diagram of PR Controller

The block diagram of PR controller is shown in fig.8.

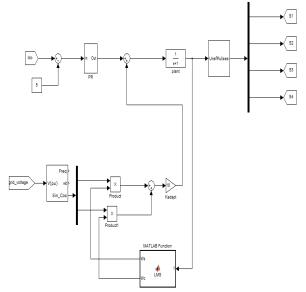


Figure 8: block diagram of PR controller

PR controller means proposed resonant controller. It gives the values of $K_p=3$, $K_T=594$.

6.Grid Connected Single-Phase PV Inverter with PRI Controller

The grid connected single-phase PV inverter with PRI controller is shown in figure 9.

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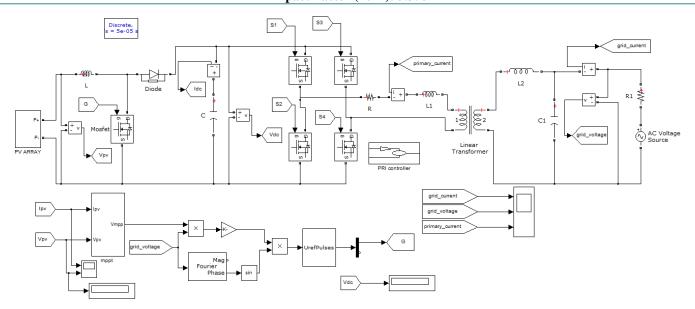


Figure 9: Grid connected single-phase PV inverter with PRI controller

The Fig.9. Consists of PV array, boost converter, singlephase inverter, an inductive filter& a step-up transformer for interfacing with the grid. The PV array gives the values are $v_{pv}=29.62V$, $I_{pv}=9.903A$, $v_{ee}=43.77v$. The boost converter boost up the voltage & current. The capacitor is used to the purpose of continuous current flowing. The inverter converts DC power to AC power. In the inverter each IGBT has resistance of 0.1Ω . The transformer having nominal power of 150VA; frequency operating is 50HZ, $L=500e^{-2}, L_1 = 100e^{-6}, L_2 = 1e^{-2},$ C=6600g-6,C1=200g-6, $R=200\Omega, R=0.1\Omega.In$ voltage ac source, peak amplitude=230V.it is also consists of PRI controller.

6.1 Block diagram of PRI controller

The block diagram of PRI controller is shown in fig.10.

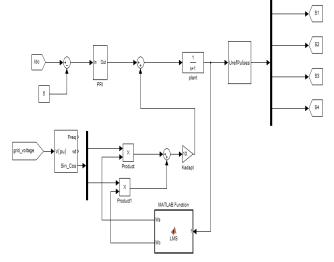
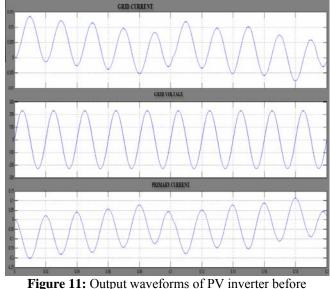


Figure 10: Block diagram of PRI controller

From the Fig.10 PRI controller means Proportional Resonant Integral controller. In this controller the values of $\kappa_p=3$, $\kappa_p=594$, $\kappa_l=100$.

6.2 Simulink results of Grid connected single-phase PV inverter before compensation

The Simulink results of namely PV current, PV voltage, Grid current, Grid voltage, Primary current of Grid connected single-phase PV inverter before compensation are shown in the Fig.11.



compensation

From the output waveforms Shown in Fig.11. It is observed that the grid current, grid voltage & primary current are in sinusoidal form. Total harmonic distortion in Grid current of single-phase PV inverter before compensation. It is observed that the THD in Grid current is 9.14%.

6.4 Simulink results of Grid connected single-phase PV inverter with PR controller

The Output waveforms of Grid connected single-phase PV inverter with PR controller is shown in fig.12.

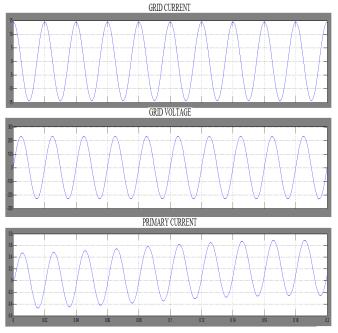


Figure 12: output waveforms of Grid connected single-phase PV inverter with PR controller

From the output waveforms Shown in Fig.12. It is observed that the grid current, grid voltage & primary current are in sinusoidal form. Total harmonic distortion in Grid current of single-phase PV inverter with PR controller. It is observed that the THD in Grid current is 2.90%.

6.5 Simulink results of Grid connected single-phase PV inverter with PRI controller

The Output waveforms of Grid connected single-phase PV inverter with PRI controller is shown in fig.13.

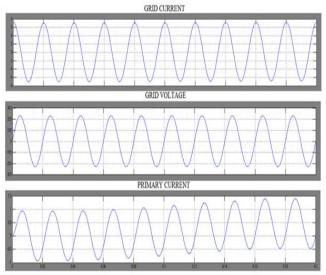


Figure 13: Output waveforms of Grid connected singlephase PV inverter with PRI controller.

From the output waveforms Shown in Fig.13. It is observed that the grid current, grid voltage & primary current are in sinusoidal form. Total harmonic distortion in Primary current of single-phase PV inverter with PRI controller. It is observed that the THD in Grid current is 1.85%.

Table 1: THD Analysis

	Total Harmonic Distortion (THD) (%)			
	Grid current	Grid voltage	Primary current	
Before compensation	9.13	0	9.14	
PR controller	0.16	0	2.91	
PRI controller	0.03	0	1.85	

The proposed single-phase PV inverter is simulated using MATLAB/SIMULINK. The corresponding results are showed before compensation, during PR& PRI controllers. Then the THD analyses for the proposed single-phase PV inverter with compensation and without compensation are compared.

7. Conclusion

Modification to the inverter current control for a grid connected single-phase photovoltaic inverter has been proposed in this paper, for ensuring high quality of the current injected into the grid. For the power circuit topology considered, the dominant causes for lower order harmonic injection are identified as the distorted transformer magnetizing current and the dead time of the inverter. It is also shown that the presence of dc offset in control loop results in even harmonics in the injected current for this topology due to the dc biasing of the transformer. A novel solution is proposed to attenuate all the dominant lower order harmonics in the system. The proposed method uses an LMS adaptive filter to estimate a particular harmonic in the grid current that needs to be attenuated. The estimated current is converted into an equivalent voltage reference using a proportional controller and added to the inverter voltage reference. The design of the gain of a proportional controller to have an adequate harmonic compensation has been explained. To avoid dc biasing of the transformer, a novel PRI controller has been proposed and its design has been presented. The interaction between the PRI controller and the adaptive compensation scheme has been studied. It is shown that there is minimal interaction between the fundamental current controller and the methods responsible for dc offset compensation and adaptive harmonic compensation. The PRI controller and the adaptive compensation scheme together improve the quality of the current injected into the grid.

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